

Supplementary Information for
Ambipolar device simulation based on the drift-diffusion model in ion-gated
transition metal dichalcogenide transistors

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I. SCHARFETTER-GUMMEL DISCRETIZATION

For the numerical calculation, we use the Scharfetter-Gummel discretization to solve the DD equation which is modified under the generalized Einstein relation. The Scharfetter-Gummel discretization is adopted for the unified mesh since it is suitable to determine carrier concentration whose change is precipitous. Assuming that $\nabla\Phi$ and $J_{n(p)}$ are constant between the meshes x_i and x_{i+1} , the current densities for electrons and holes are written as

$$J_n(x_{i+1/2}) = -q\mu_e \frac{\Phi(x_{i+1},0) - \Phi(x_i,0)}{dx} \frac{n(x_{i+1}) - \exp\left\{\frac{\Phi(x_{i+1},0) - \Phi(x_i,0)}{V_T \sqrt{g_n(x_i)g_n(x_{i+1})}}\right\} n(x_i)}{1 - \exp\left\{\frac{\Phi(x_{i+1},0) - \Phi(x_i,0)}{V_T \sqrt{g_e(x_i)g_n(x_{i+1})}}\right\}}, \quad (1)$$

$$J_p(x_{i+1/2}) = -q\mu_h \frac{\Phi(x_{i+1},0) - \Phi(x_i,0)}{dx} \frac{p(x_i) - \exp\left\{\frac{\Phi(x_{i+1},0) - \Phi(x_i,0)}{V_T \sqrt{g_h(x_i)g_h(x_{i+1})}}\right\} p(x_{i+1})}{1 - \exp\left\{\frac{\Phi(x_{i+1},0) - \Phi(x_i,0)}{V_T \sqrt{g_p(x_i)g_p(x_{i+1})}}\right\}}, \quad (2)$$

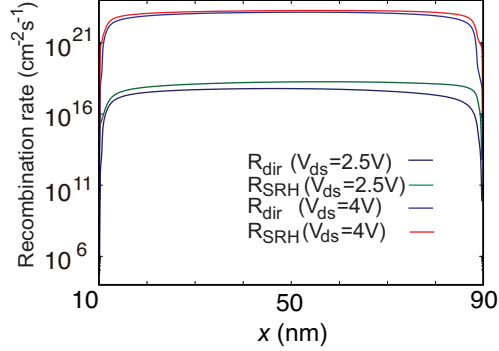
where $V_T = k_B T/q$. Then, the continuity equations for electrons and holes are written as

$$\begin{aligned} \frac{J_n(x_{i+1/2}) - J_n(x_{i-1/2})}{x_{i+1} - x_i} &= qR(x_i), \\ \frac{J_p(x_{i+1/2}) - J_p(x_{i-1/2})}{x_{i+1} - x_i} &= -qR(x_i). \end{aligned} \quad (3)$$

Here, we determine the carrier concentration $n(x_i)$ and $p(x_i)$ using the continuity equation. Note that for the carrier with the Boltzmann distribution, $g_{n(p)}(x_i) = 1$.

II. RECOMBINATION RATE OF ELECTRONS AND HOLES IN AMBIPOLAR REGION

In Supplementary Figure 1, we plot the recombination rate of the direct recombination, R_{dir} , and the SRH recombination R_{SRH} as a function of the location x in the saturation region, $V_{\text{ds}} = 2.5\text{V}$, and the ambipolar region, $V_{\text{ds}} = 4\text{V}$. Note that R_{dir} and R_{SRH} are zero for $V_{\text{ds}} = 0.5\text{V}$, since the $p(x) = 0$ in the linear region. As shown from the figure, the electron-hole recombination is yielded in the whole channel area since the size of the recombination zone is about $1\text{ }\mu\text{m}$ which is much larger than the channel size L_x considered in this paper.

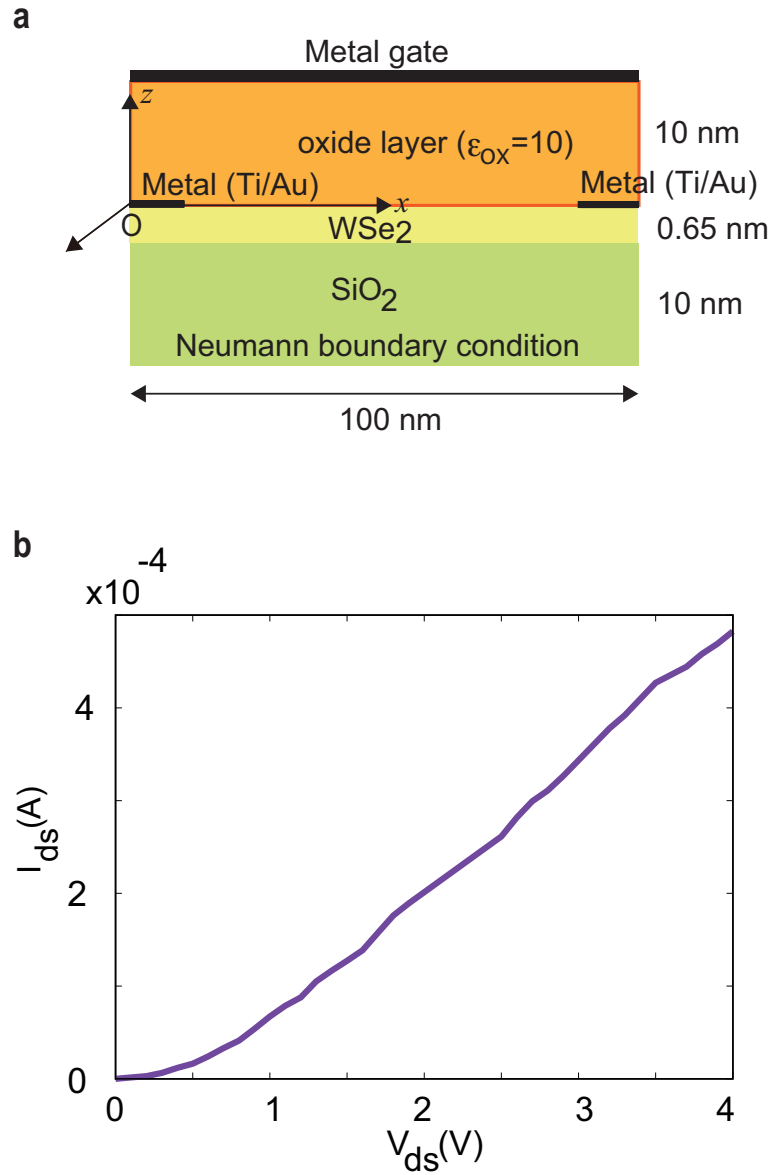


Supplementary Figure 1: Recombination rate for the direct recombination and the SRH recombination for $V_{\text{ds}} = 2.5\text{V}$ and $V_{\text{ds}} = 4\text{V}$.

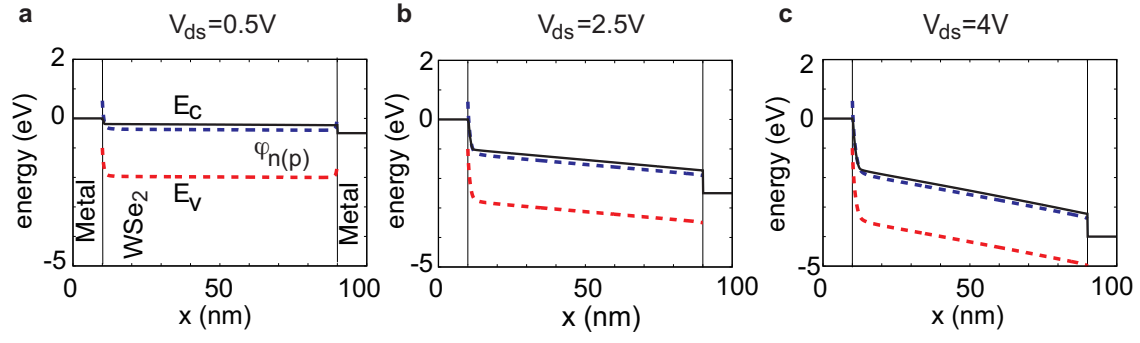
III. WSe₂ MOS TRANSISTORS

In this section, we calculate the WSe₂ transistor with the same structure as Fig. 1, but the IL is replaced with the oxide layer as shown in Supplementary Fig. 2a. Note that in experiments, the conventional WSe₂ metal-oxide-semiconductor (MOS) transistors are usually p-type due to unintentional doping. However, we adopt the intrinsic WSe₂ with the n-type contact to compare the results with the ion-gating WSe₂ transistors. The gate voltage is fixed to 10 V. The carrier concentration for the gate voltage is approximately $5 \times 10^{13}\text{ cm}^{-2}$. The oxide layer has same relative permittivity ($\epsilon_{\text{ox}} = 10$) with the one for IL. In Supplementary Fig. 2b, we plot the current as a function of V_{ds} . The current is almost linearly increasing since the threshold voltage and the pinch-off voltage for $V_{\text{gs}} = 10\text{V}$ is much larger than $V_{\text{ds}} = 4\text{ V}$. Supplementary Figure 3 shows the band profile of WSe₂ for $V_{\text{ds}} = 0.5\text{V}$, 2.5V , and 4V . For $V_{\text{ds}} = 2.5\text{V}$, and 4V , the potential drops mainly at the source region and also decreases at the channel linearly. At the drain region, the

Schottky barrier does not prevent electrons pass through the WSe₂-metal junction and acts as the perfect Ohmic contact. Since the Fermi energy of the metal contact is located inside the band gap, holes at the drain contact cannot pass through the junction. Thus, the MOS transistor is n-type up to $V_{ds} = 4V$.



Supplementary Figure 2: **a** Model of MOS transistors. **b** The current for the MOS transistor at $V_{gs} = 10V$.



Supplementary Figure 3: The band profile of **a** $V_{ds} = 0.5V$, **b** $V_{ds} = 2.5V$ and **c** $V_{ds} = 4V$ for the WSe_2 MOS transistors.