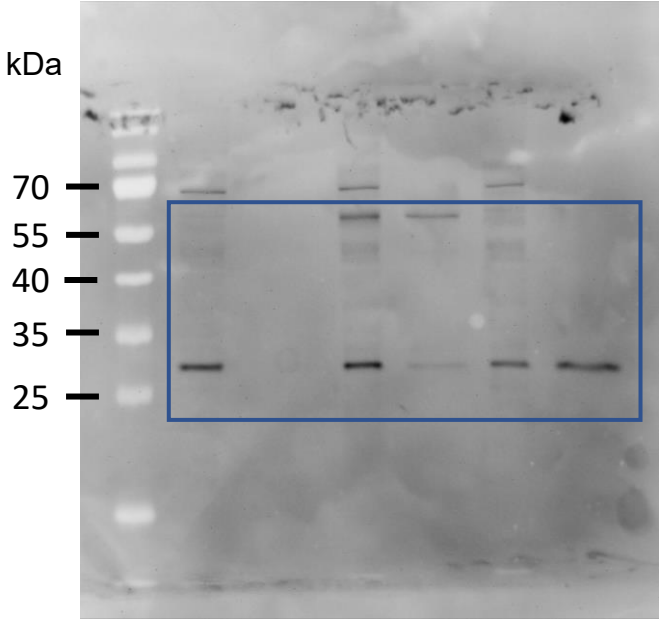


Figure S5, Panel A and B

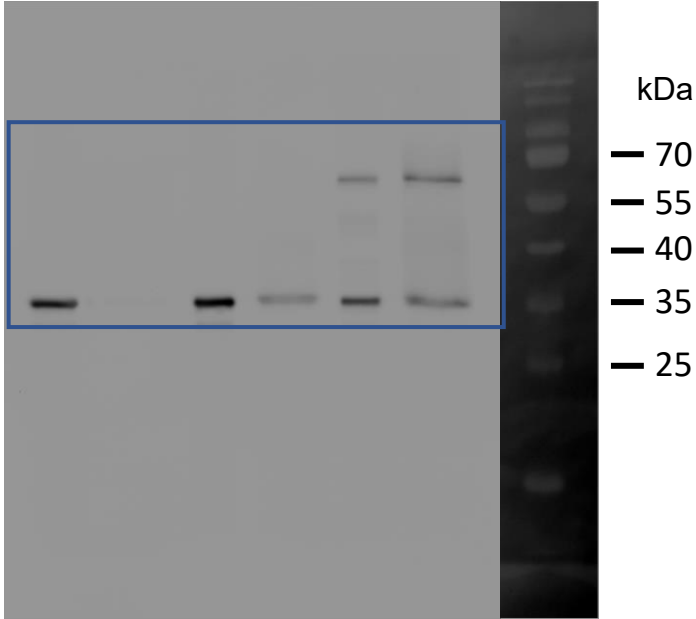
PHB1

Input & IP



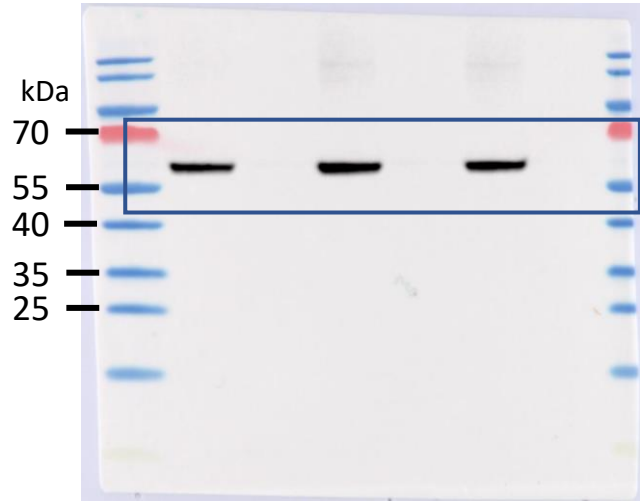
PHB2

Input & IP

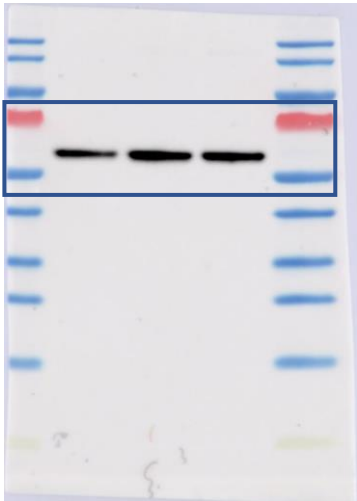


ATP5A

Input & IP

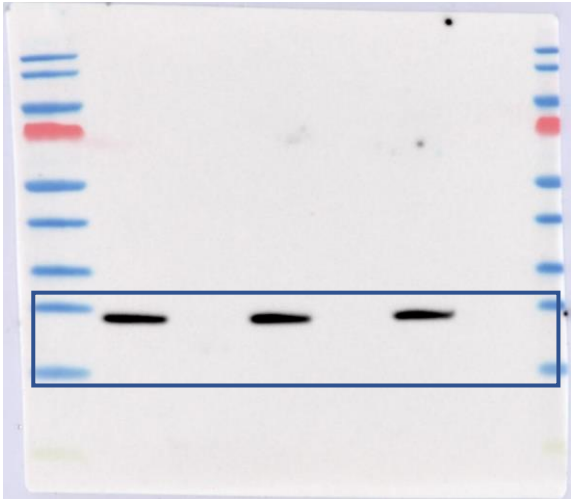


Flowthrough



COX2

Input & IP



Flowthrough

