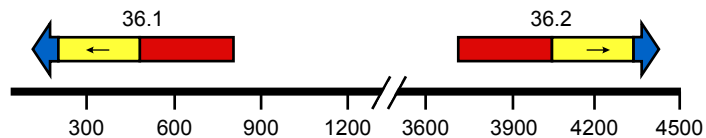


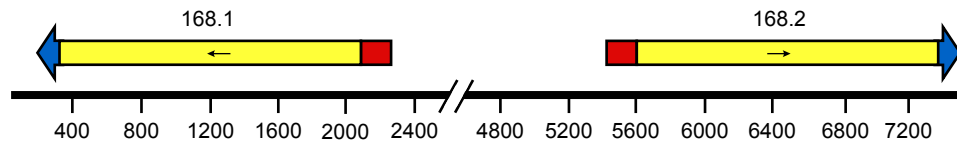
A

36 VSP Inverted pair



B

168 VSP Inverted pair



C

linear
array