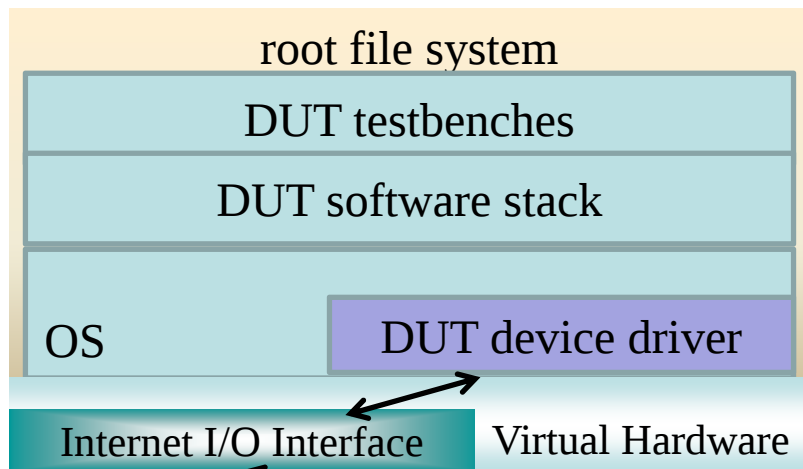
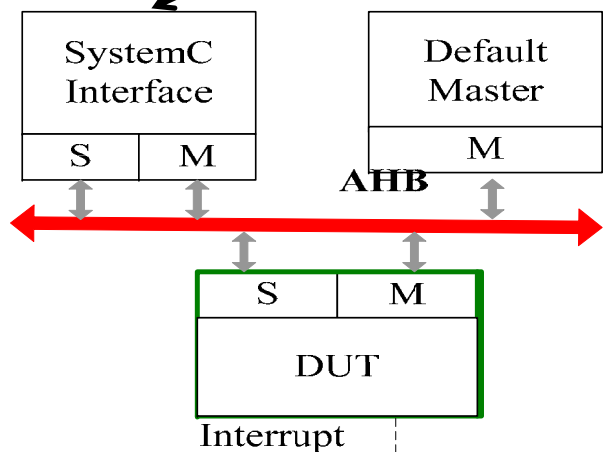


a. Architecture Development

Software
development

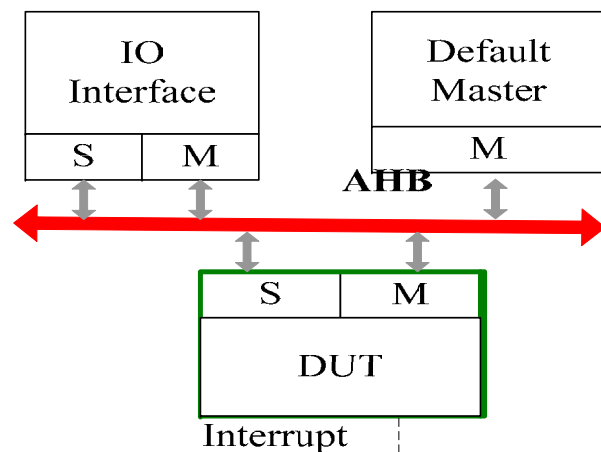
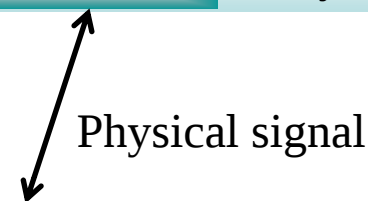
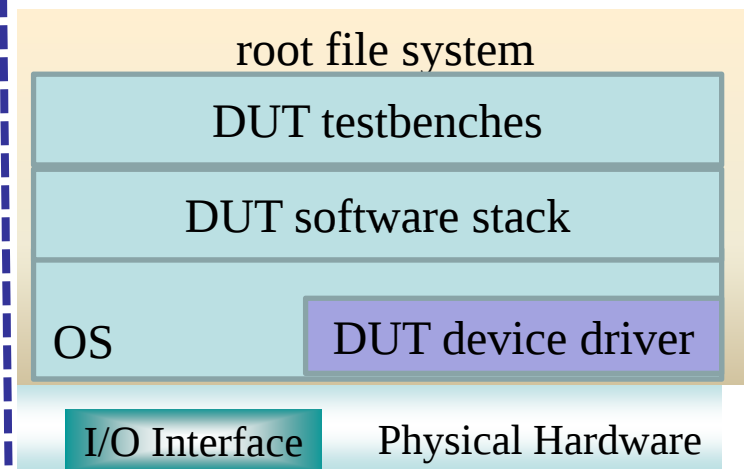


DUT



TLM/RTL

b. Implementation Development



FPGA