

Revised figure 1A

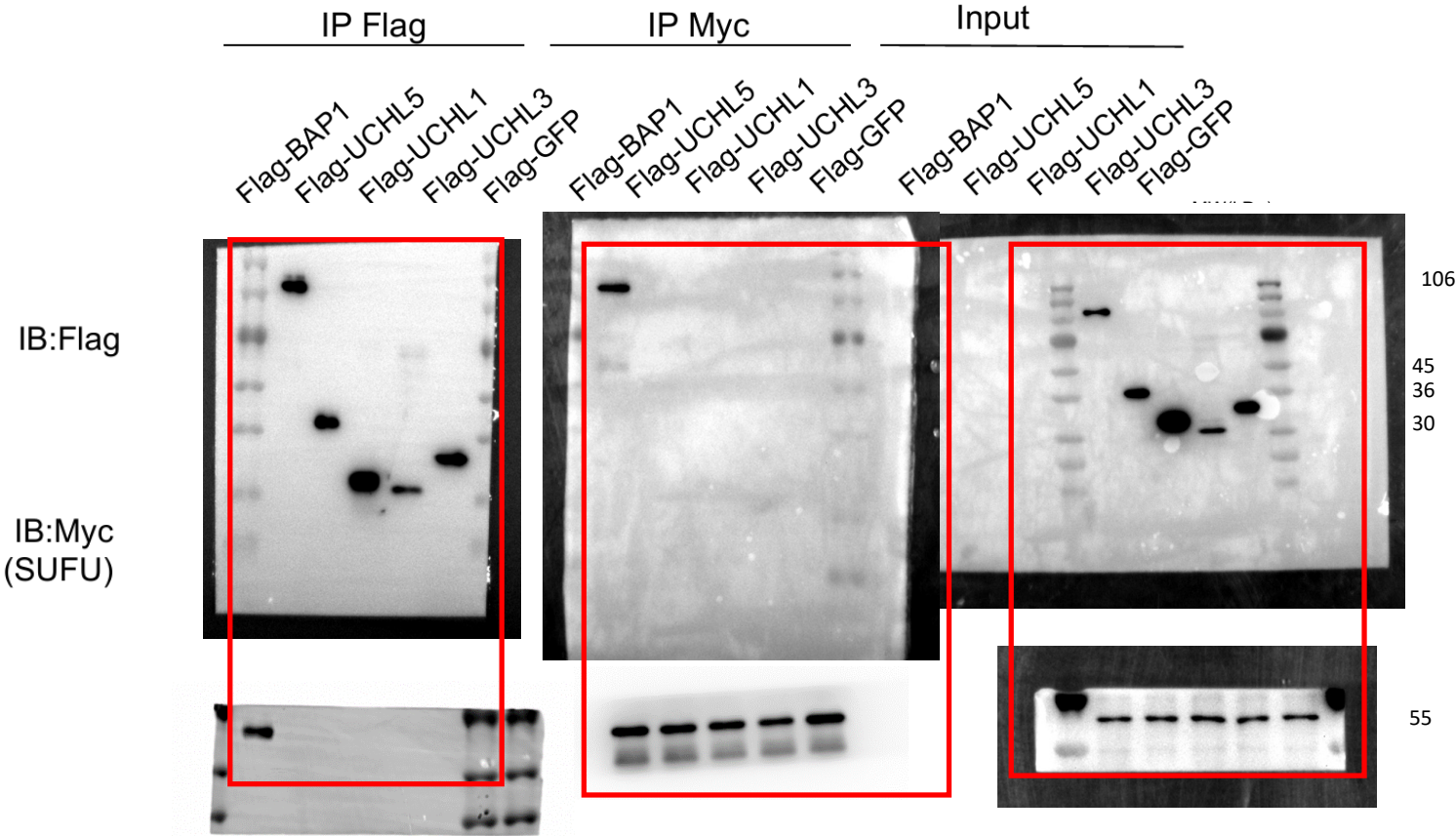


figure 1C

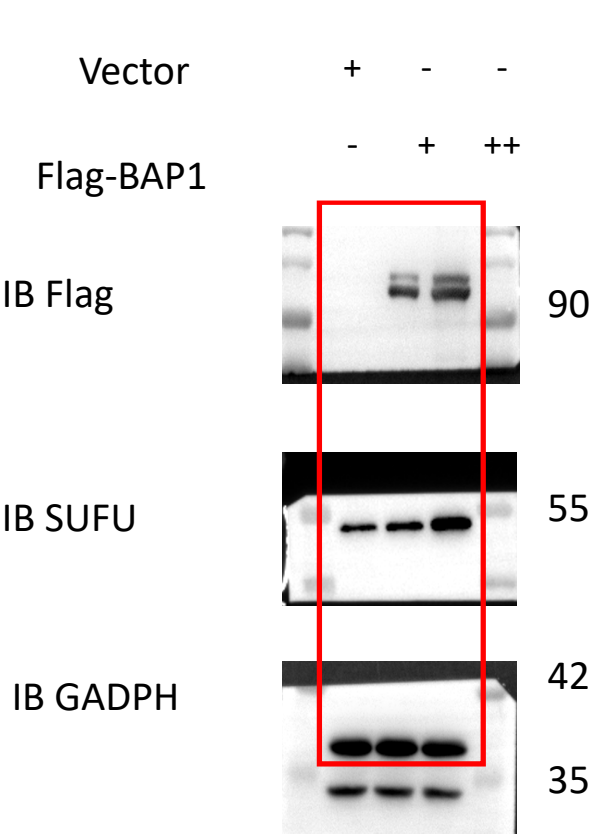


Figure 1G

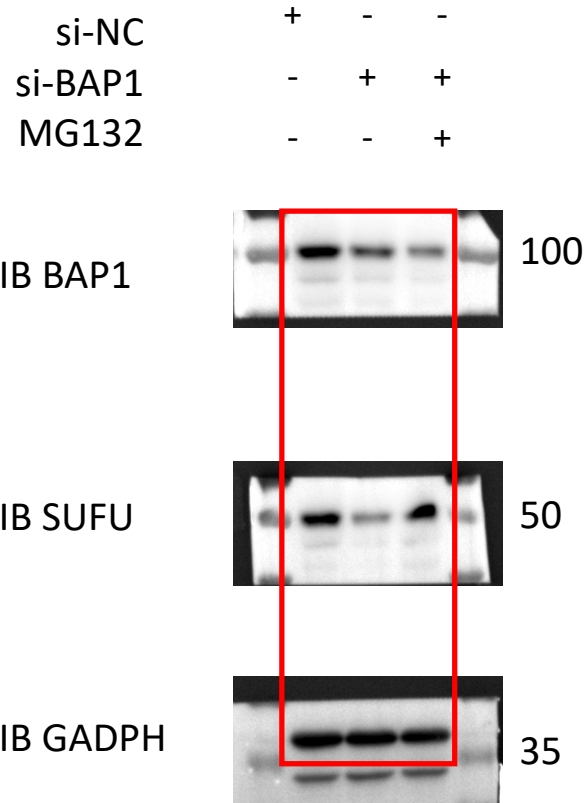
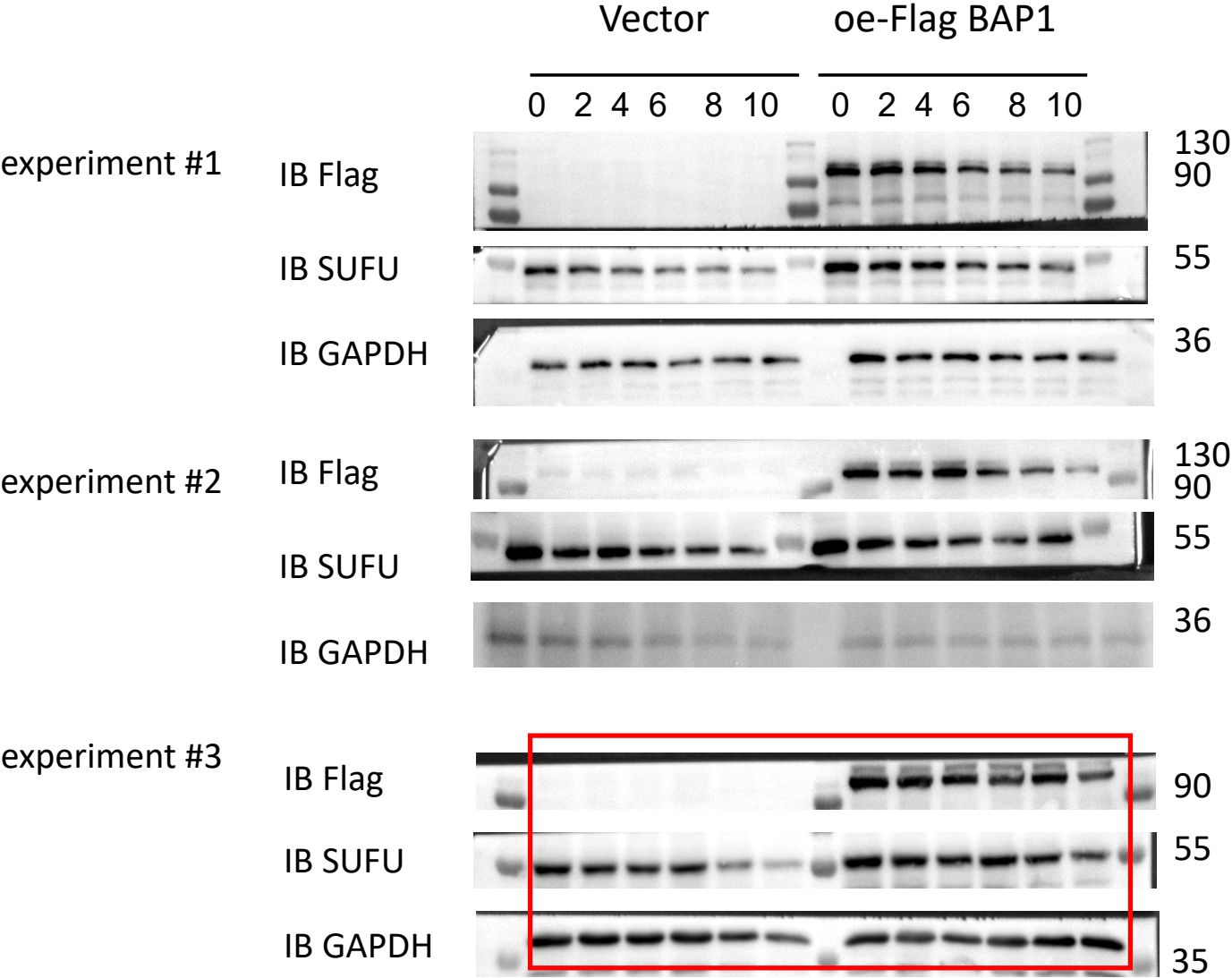


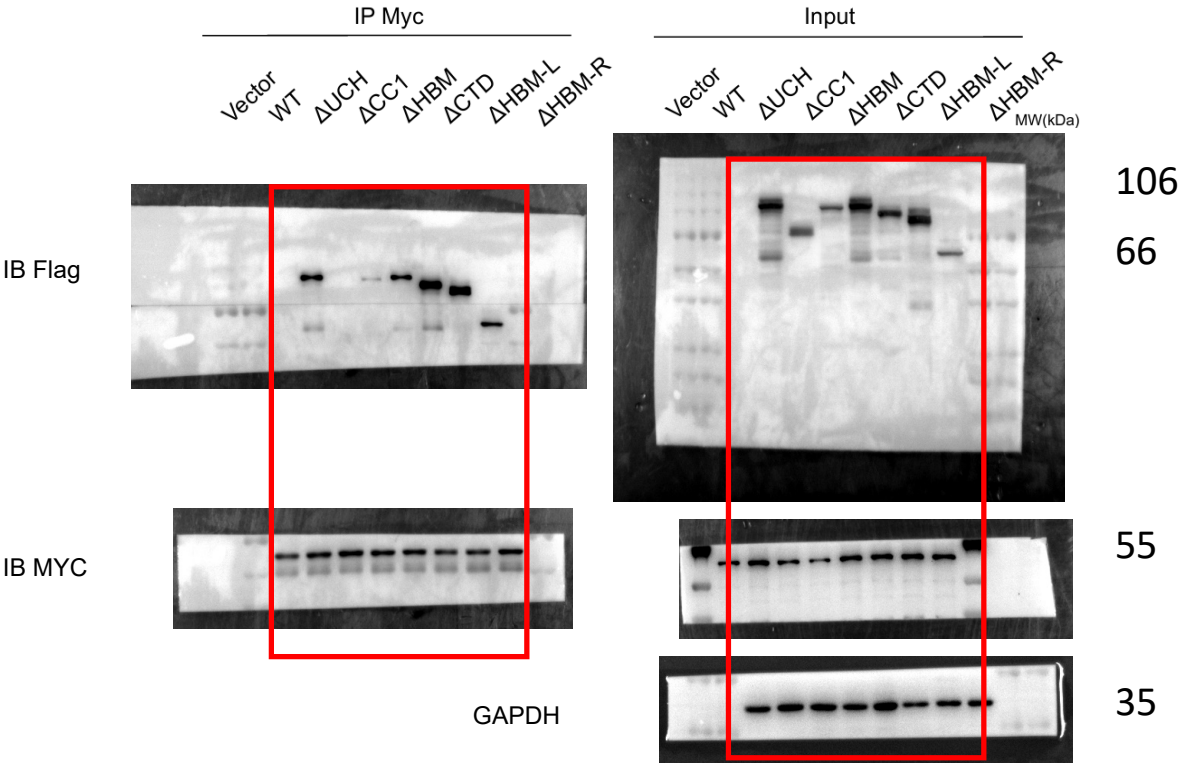
figure 1E



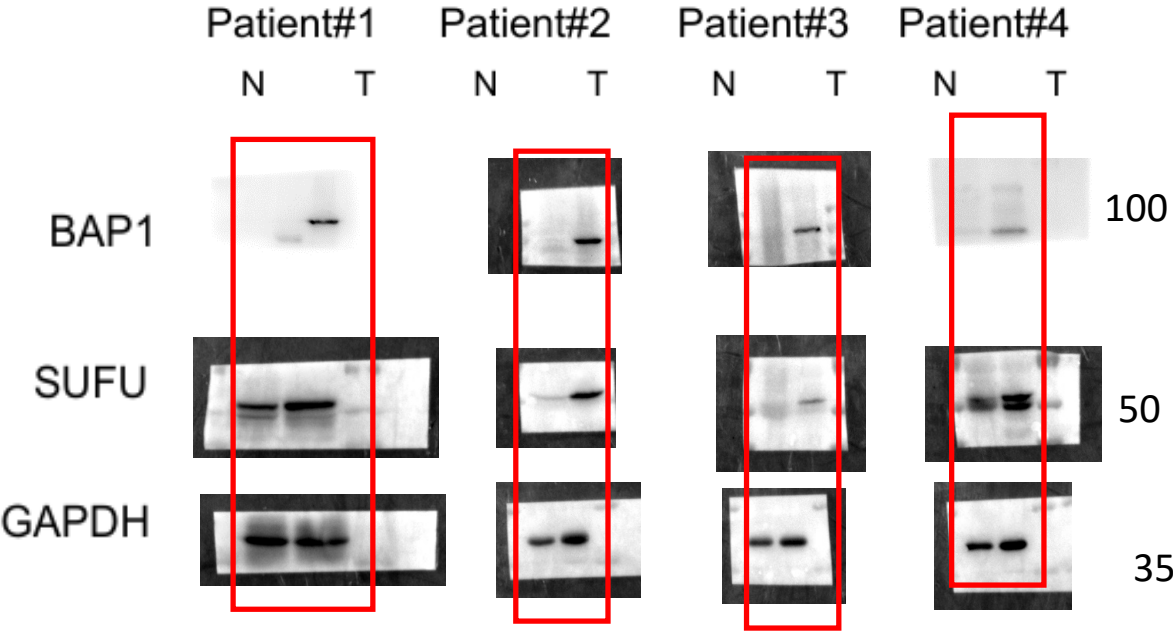
Revised figure 1H, 1I, 1J

		INPUT		IP SUFU				INPUT		IP SUFU			
		+	-	+	-			+	-	+	-		
Vector		+	-	+	-	si-NC		+	-	+	-	si-NC	
Flag-BAP1		-	+	-	+	si-BAP1		-	+	-	+	BAP-IN-1	
IB UB													180
IB SUFU													55
IB GAPDH													55
IB Flag													35
							IB BAP1						106
									IB BAP1				

Revised figure 1L



Revised figure 2B



Supplementary Figure 1C

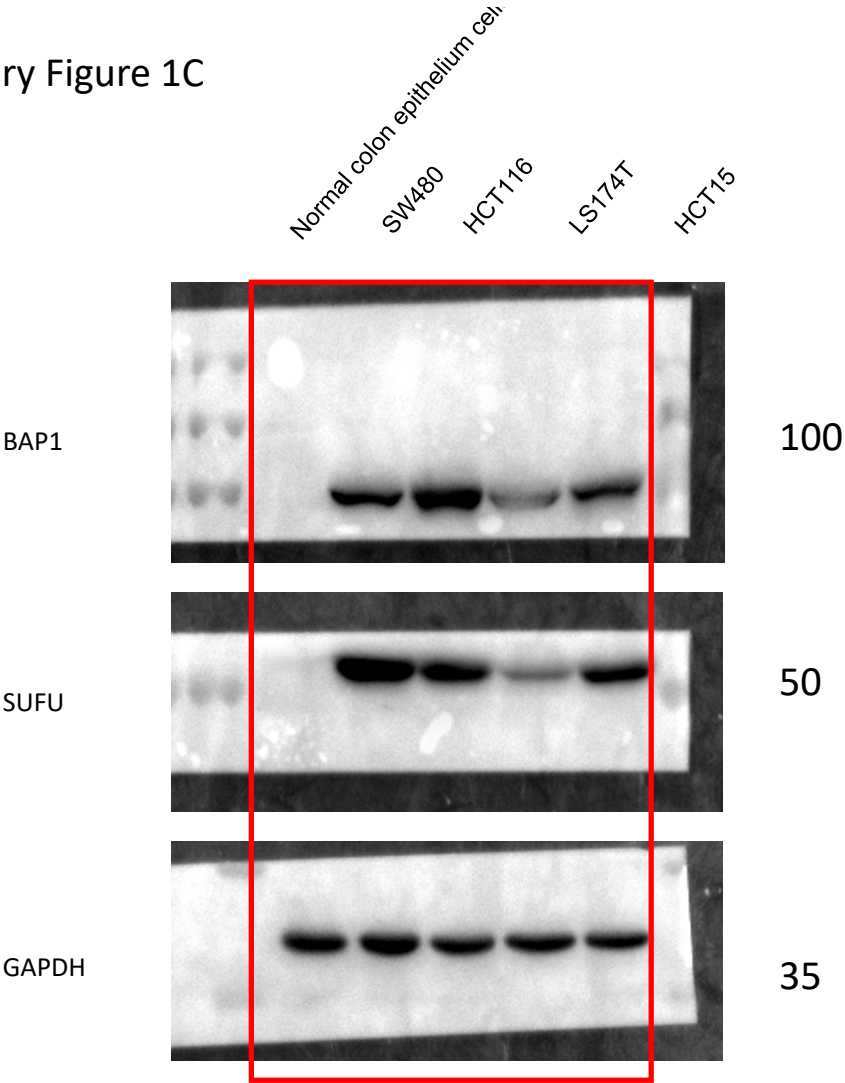


figure 3A

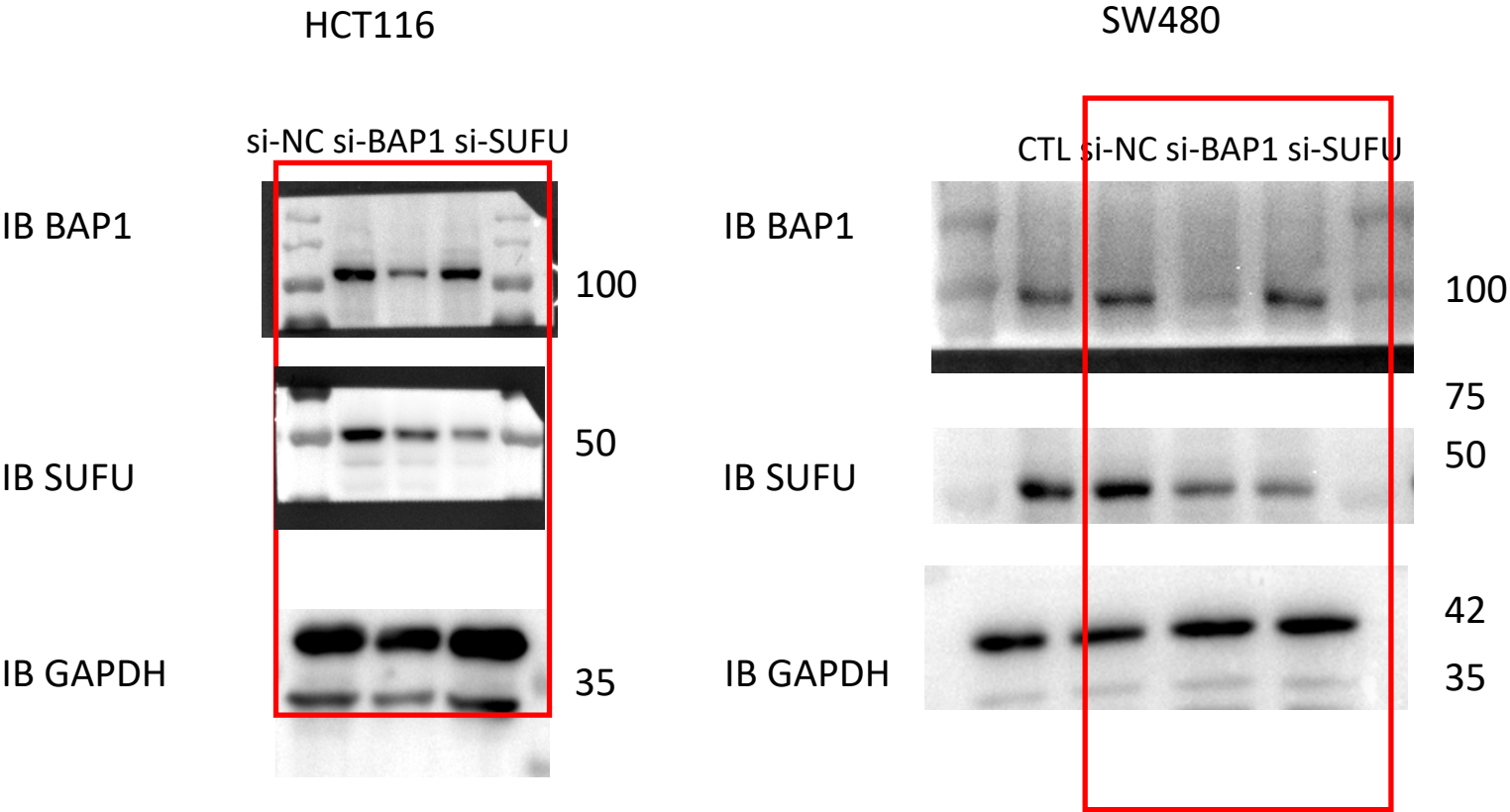


figure 3H

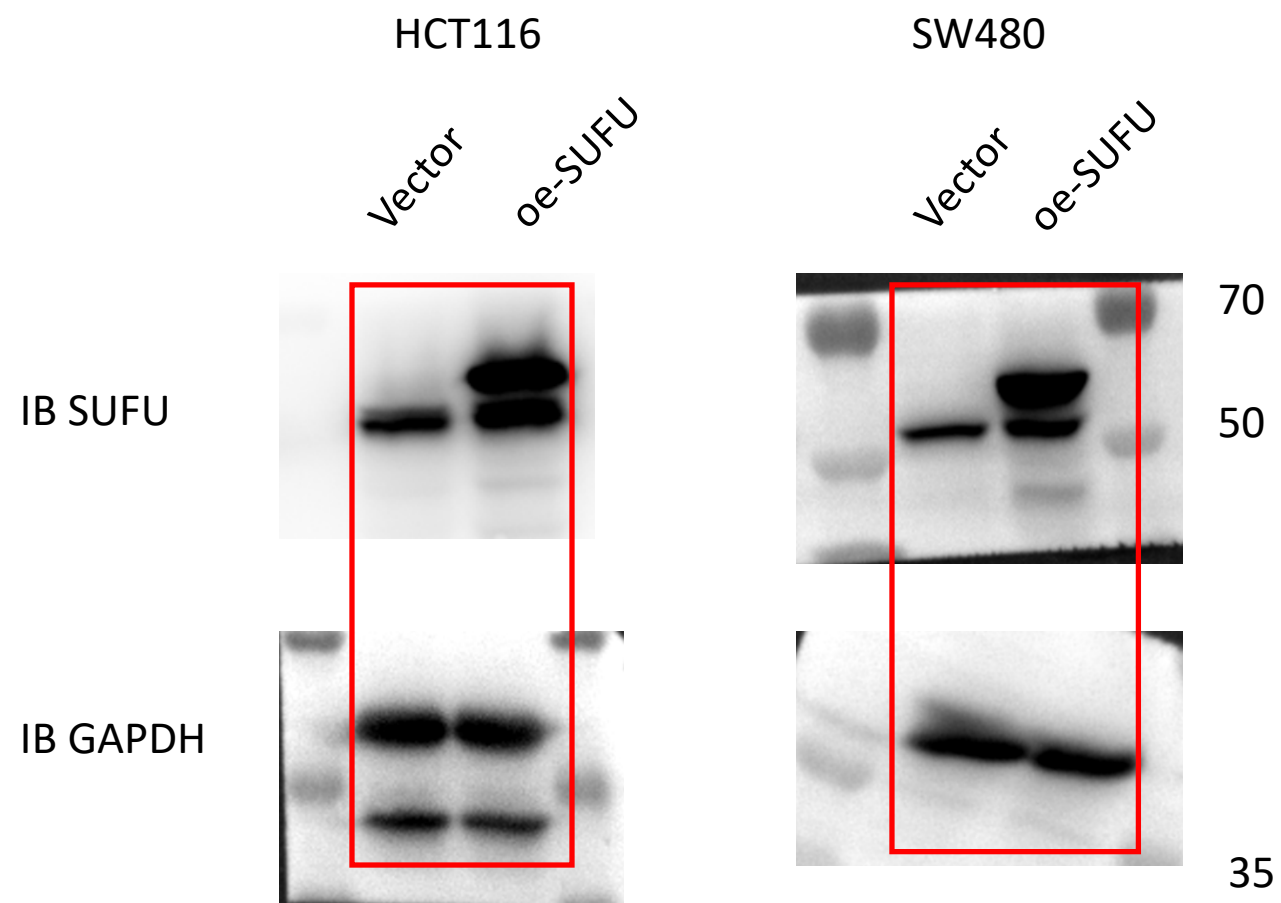


figure 3K

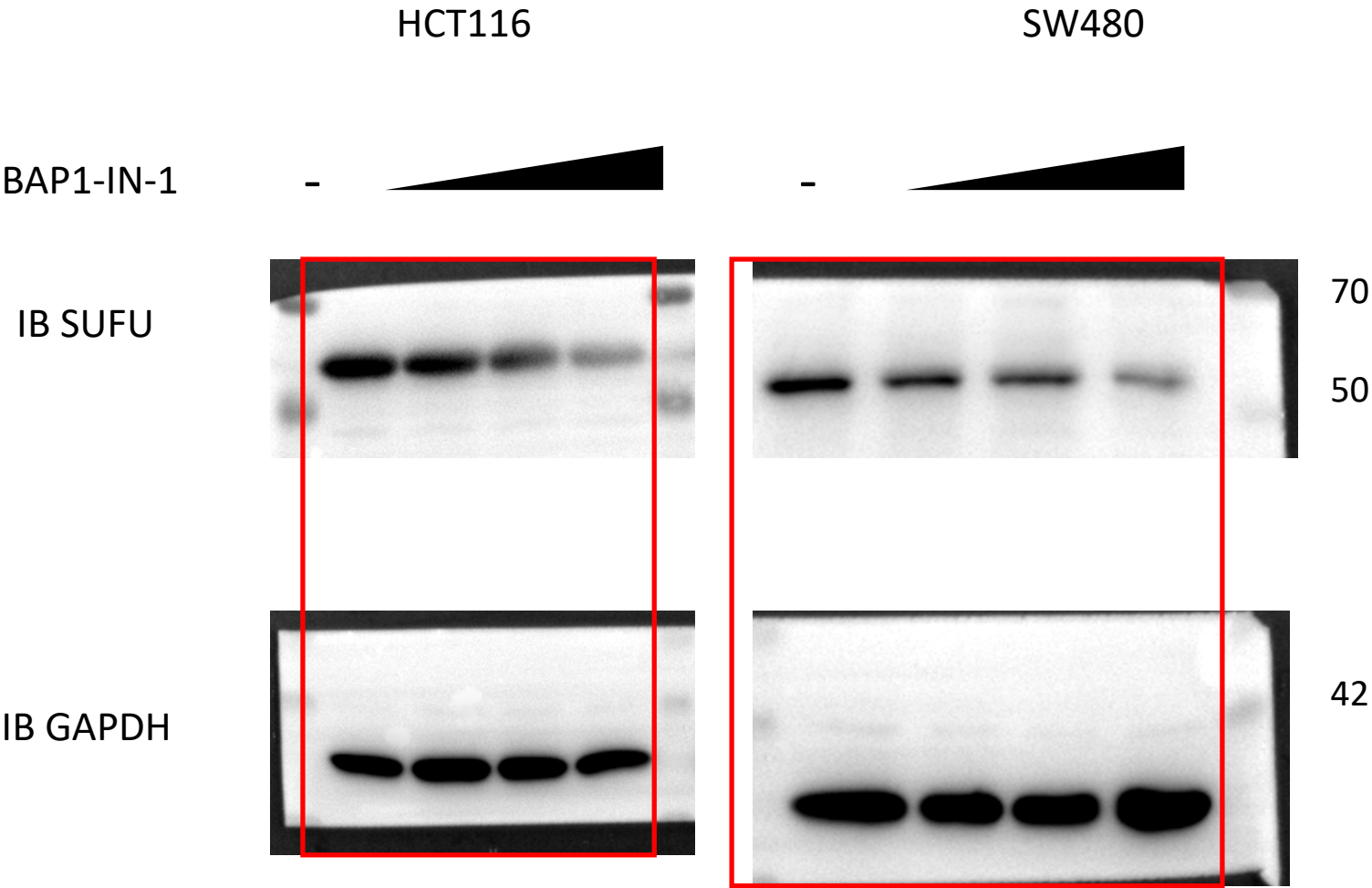


figure 4J

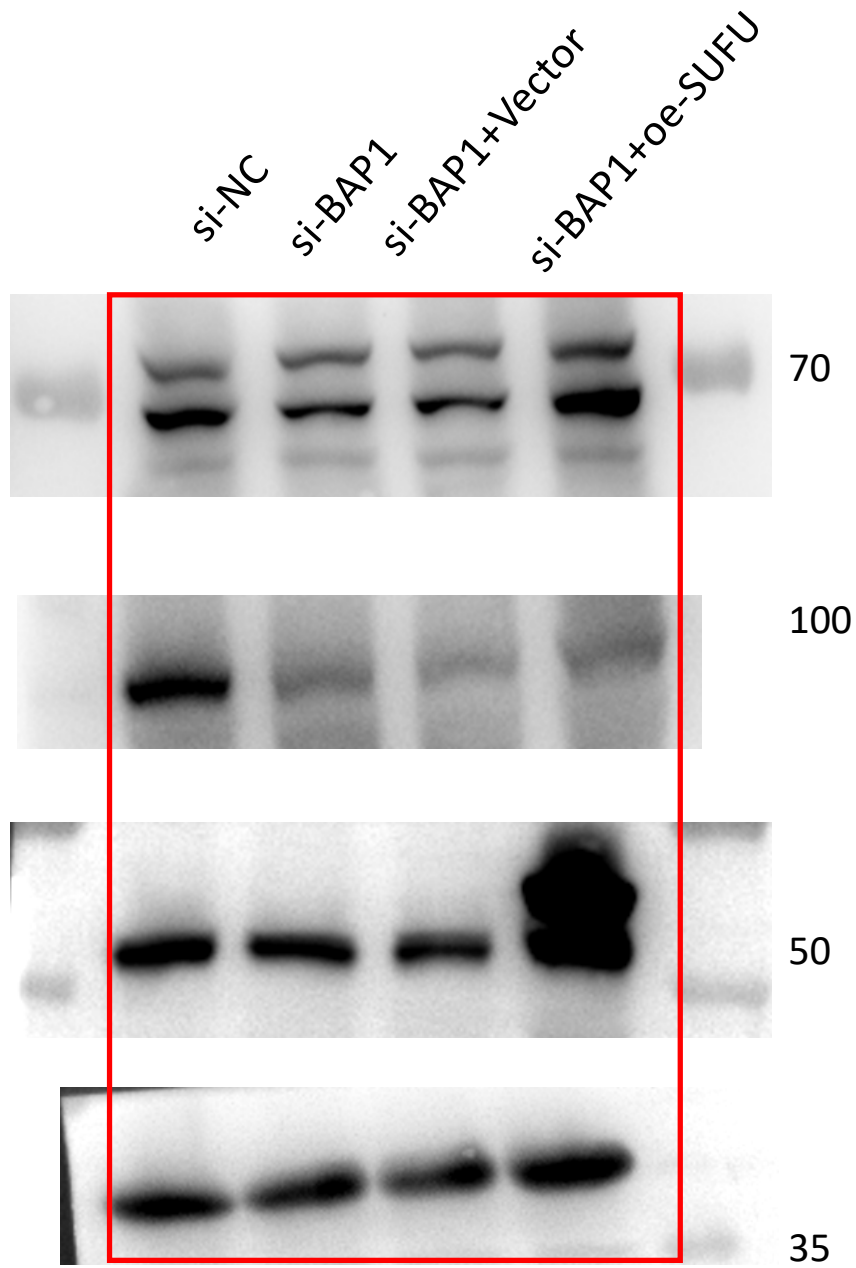
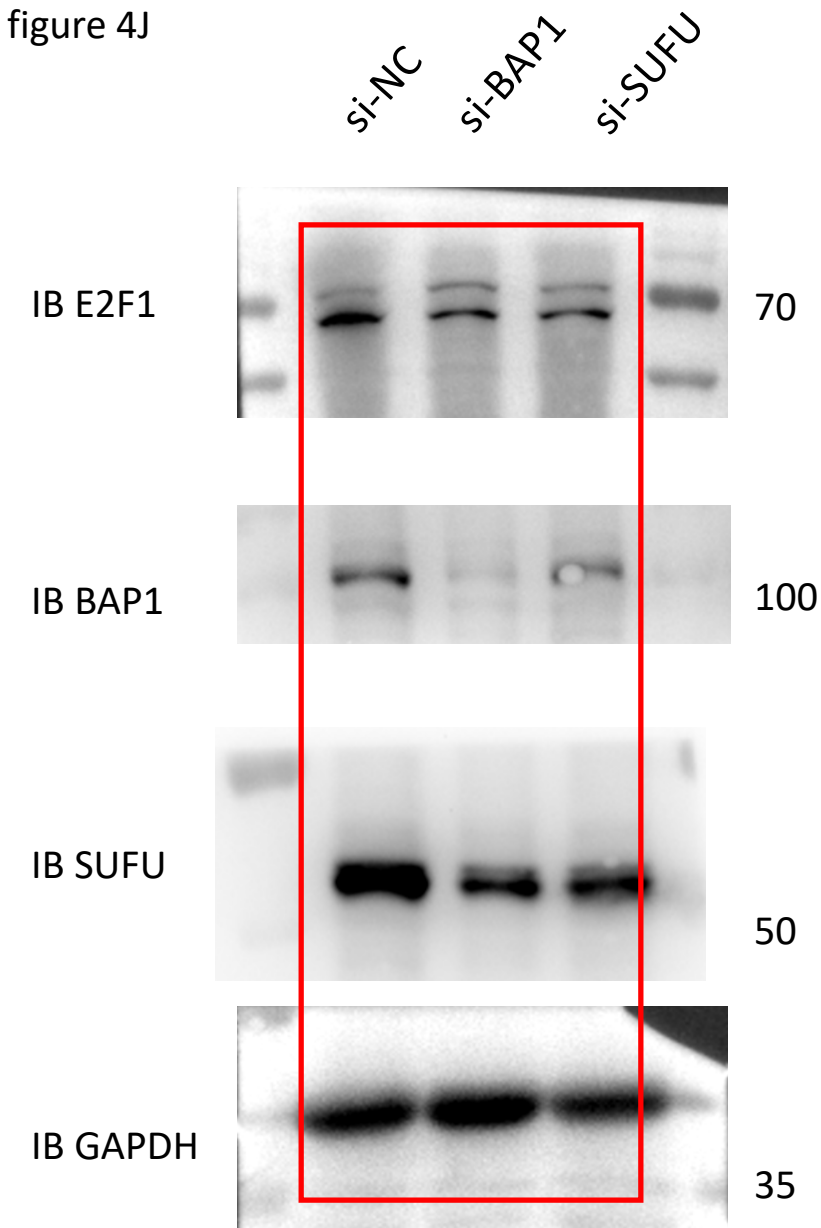


figure 5D

