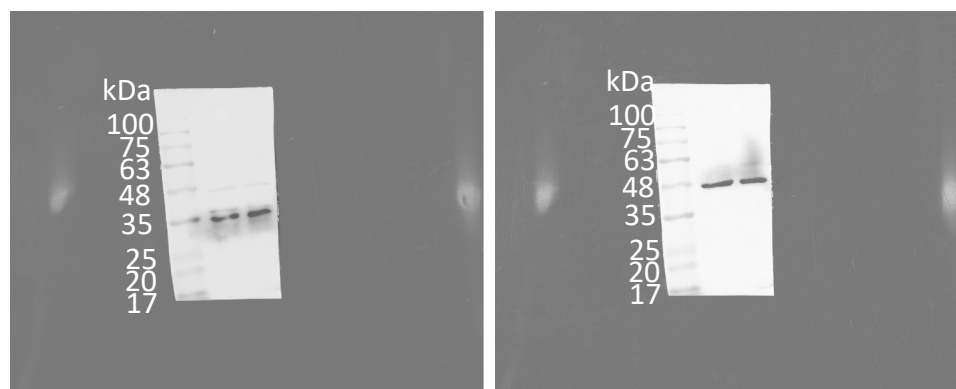
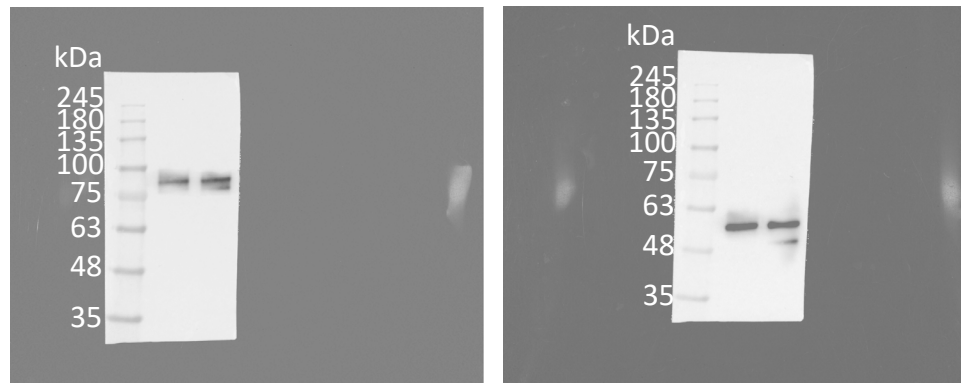
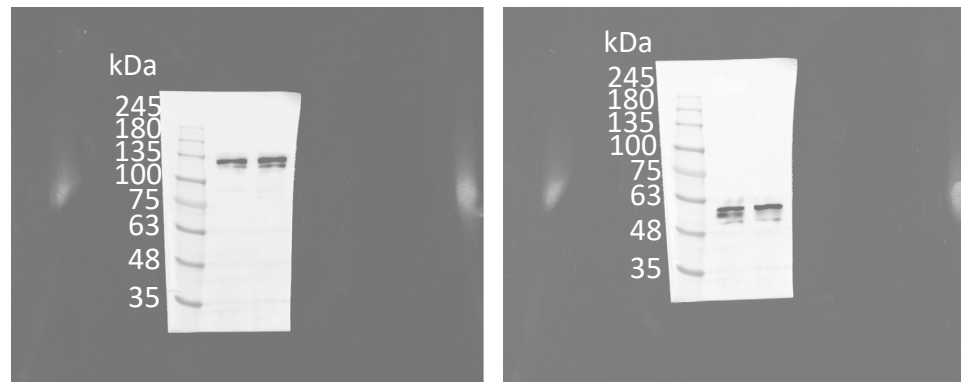




Panel B

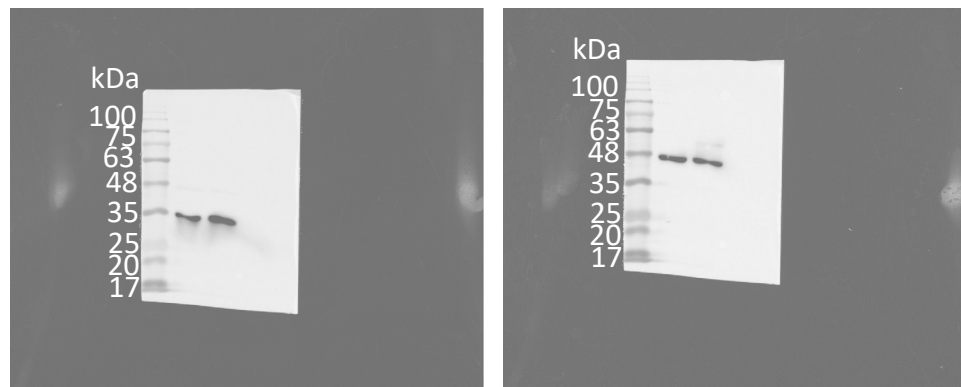


Panel C

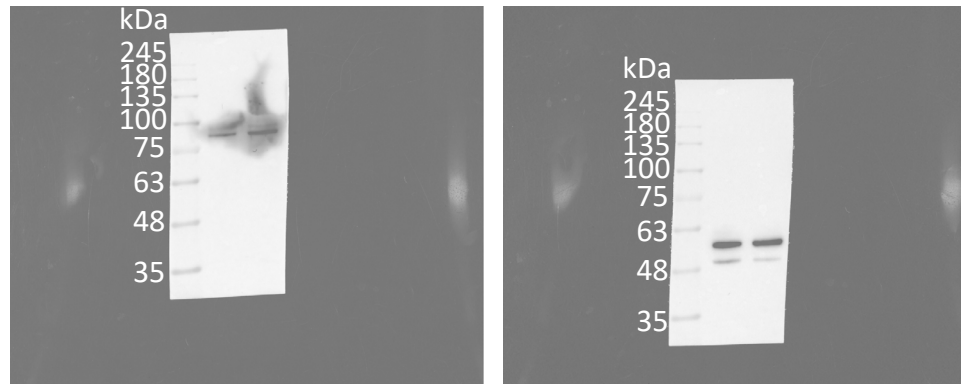


Panel D

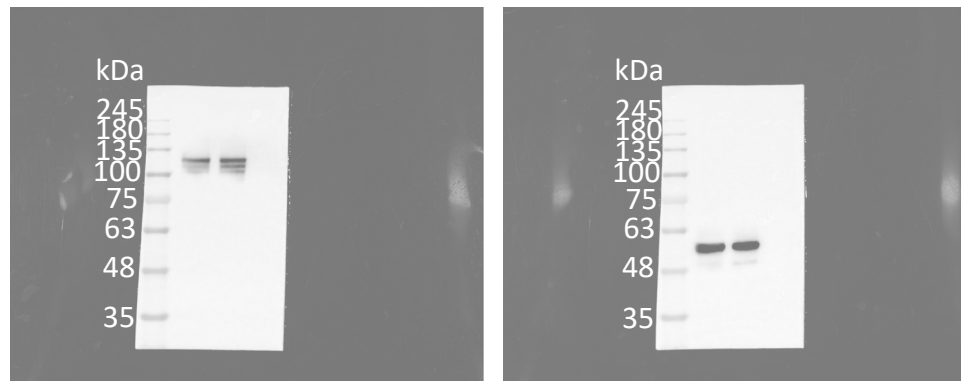
Figure S1. Panel B, C, D



Panel E

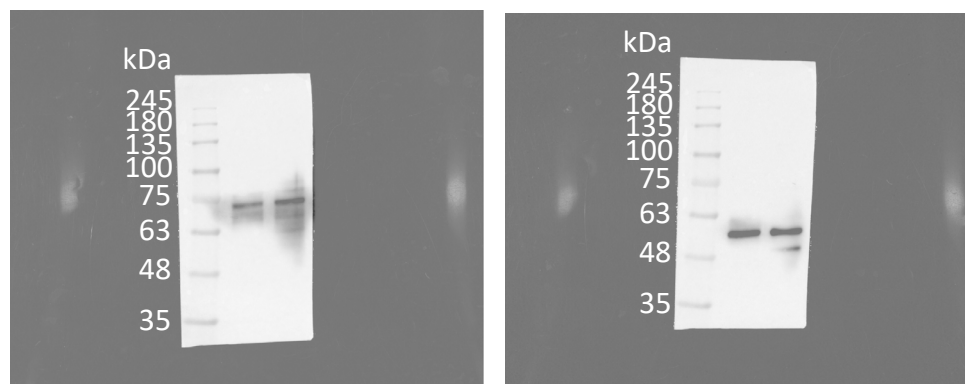


Panel F

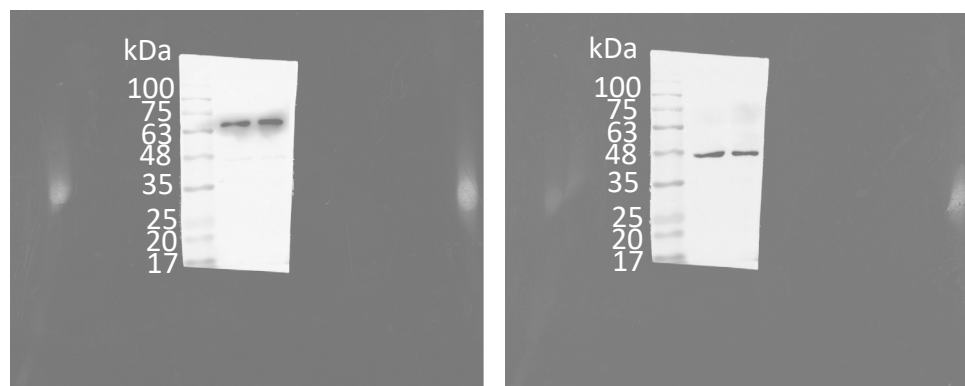


Panel G

Figure S1. Panel E, F, G

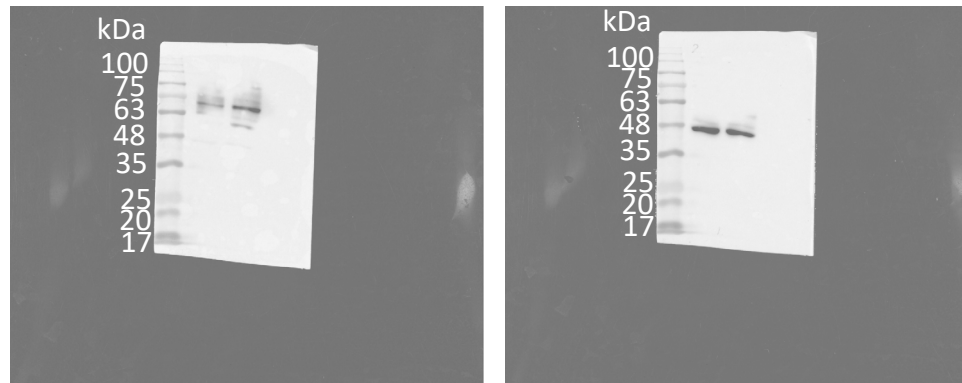


Panel H

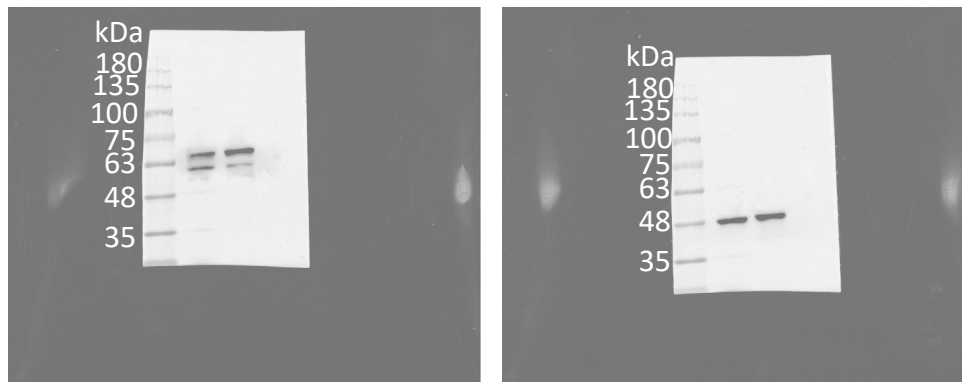


Panel I

Figure S1. Panel H, I

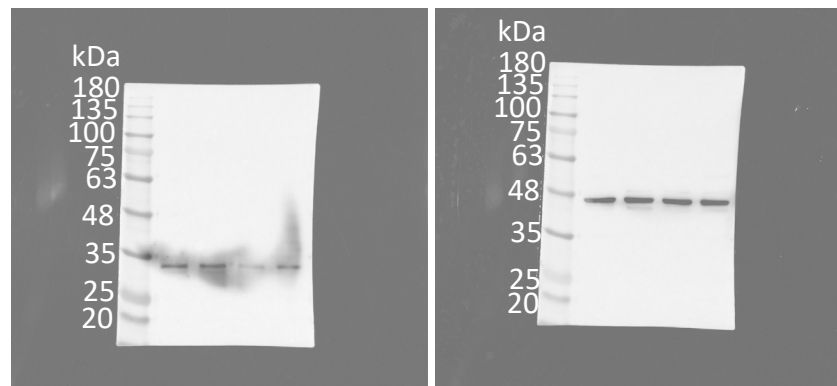


Panel J

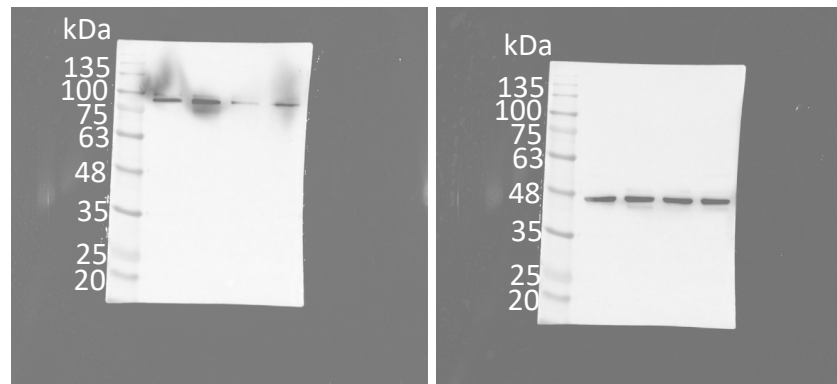


Panel K

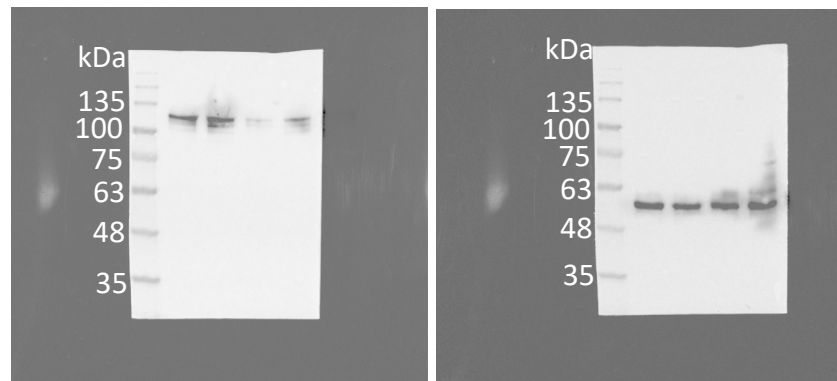
Figure S1. Panel J, K



Panel A

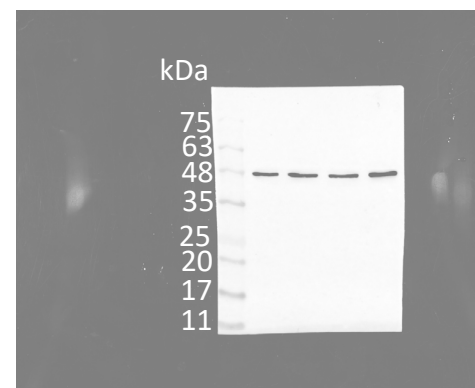
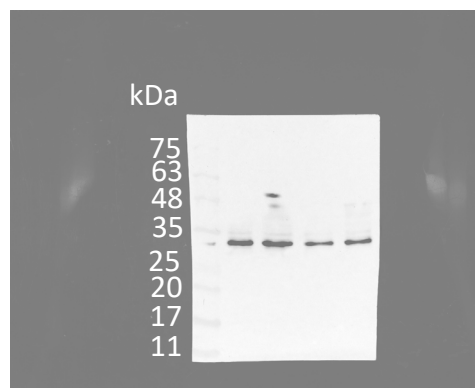


Panel B

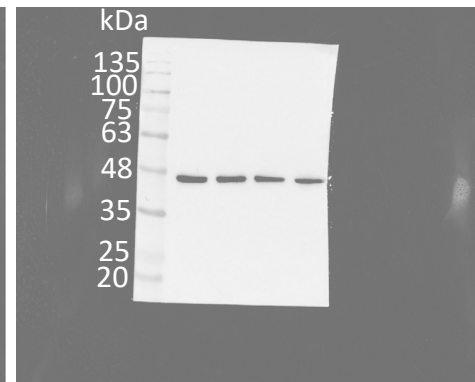
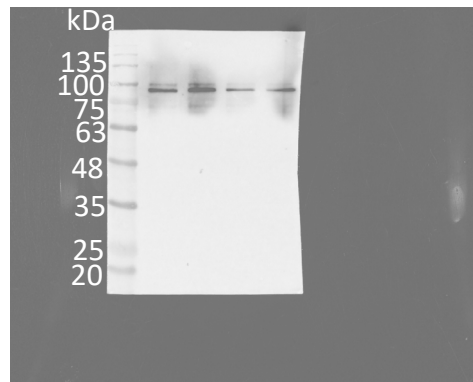


Panel C

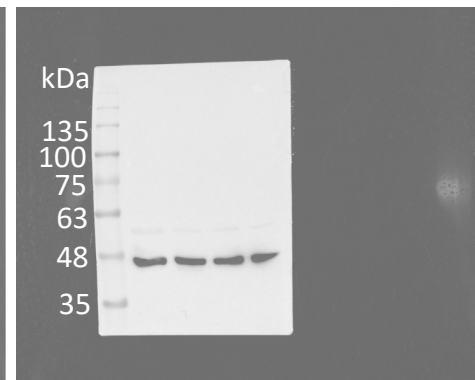
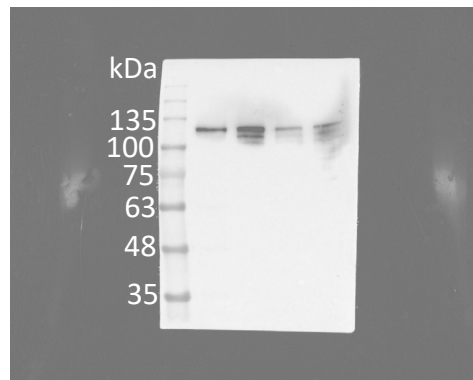
Figure S2. Panel A, B, C



Panel E

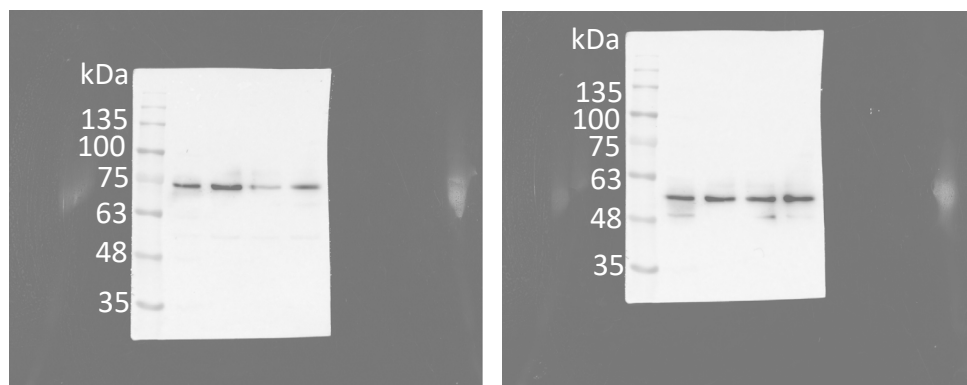


Panel F

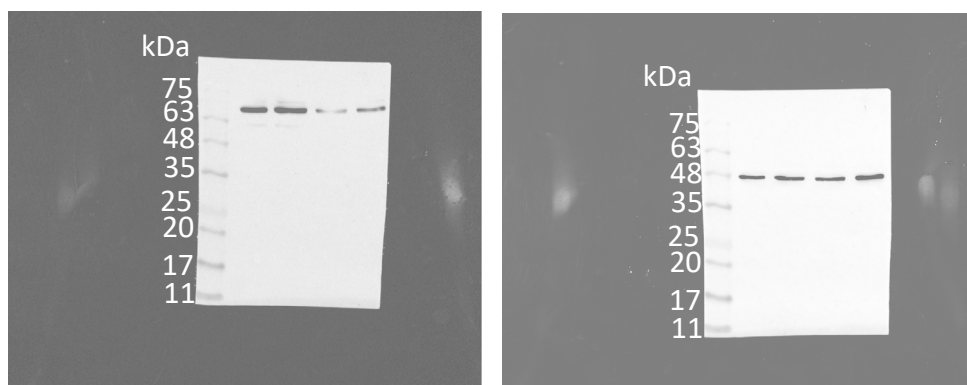


Panel G

Figure S2. Panel E, F, G

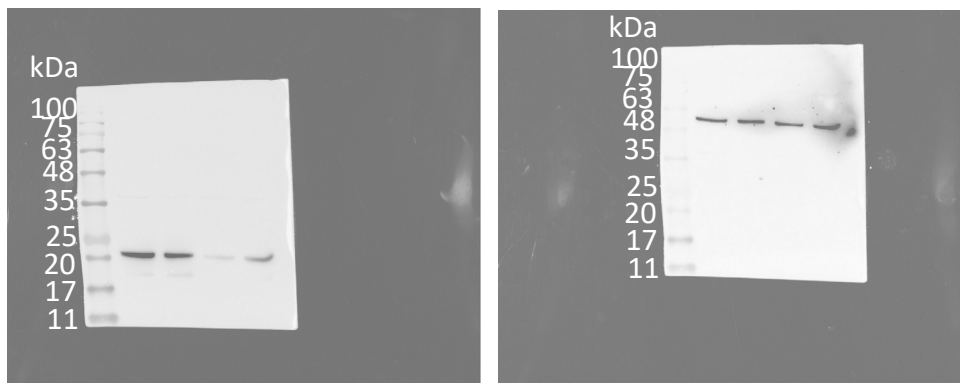


Panel D

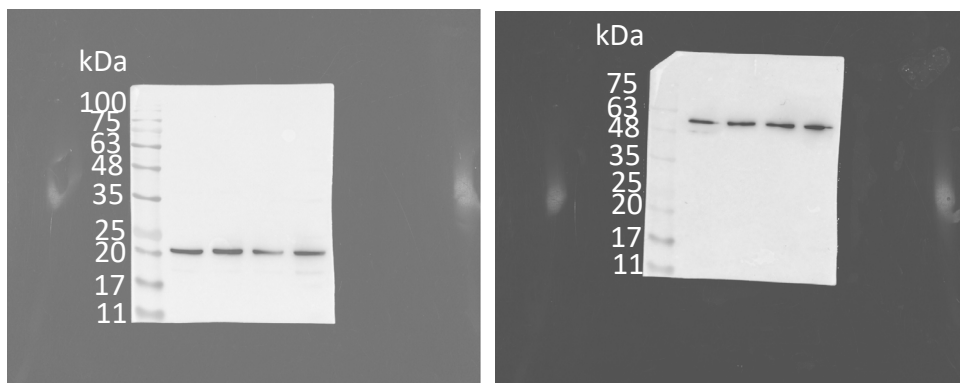


Panel H

Figure S2. Panel D, H

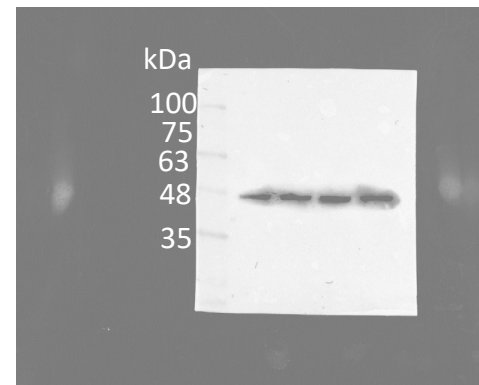
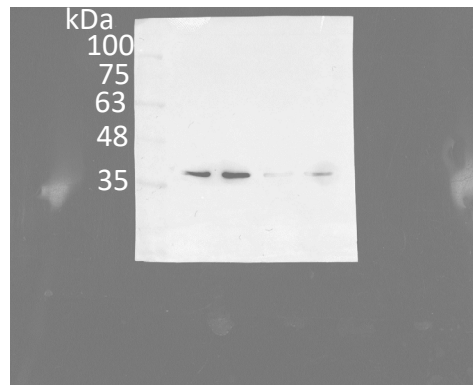


Panel C

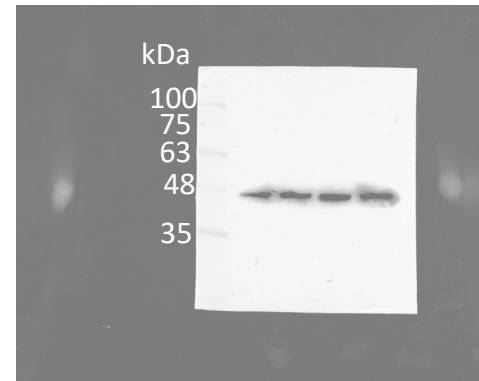
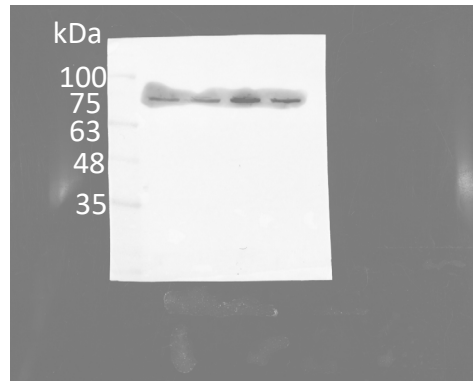


Panel F

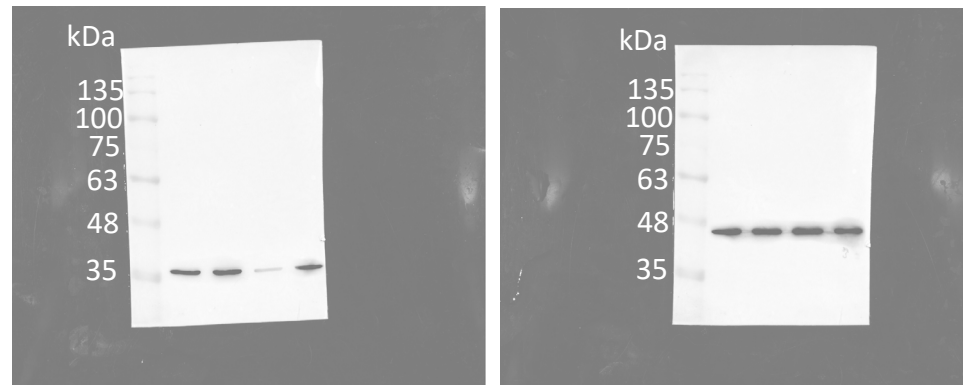
Figure S3. Panel C, F



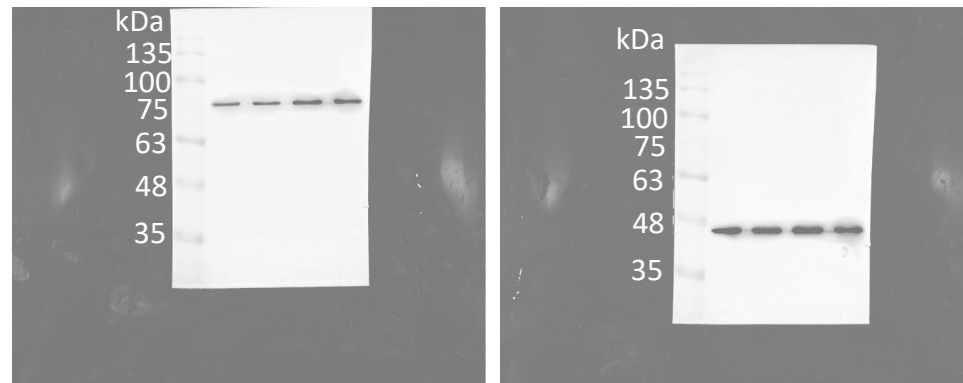
Panel K



Panel L

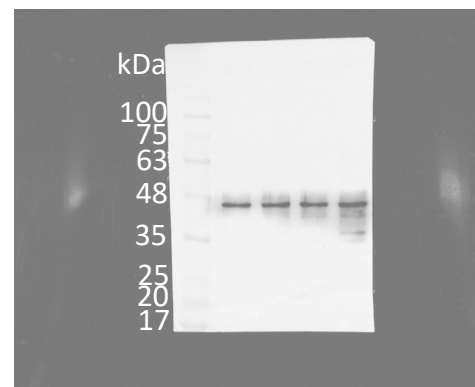
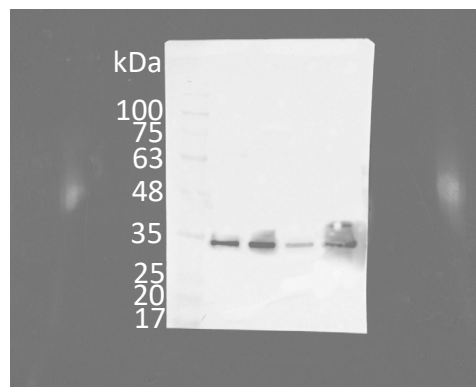


Panel Q

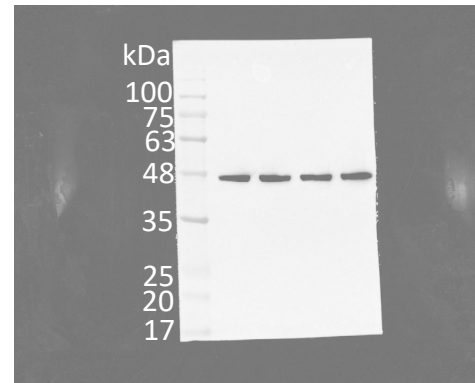
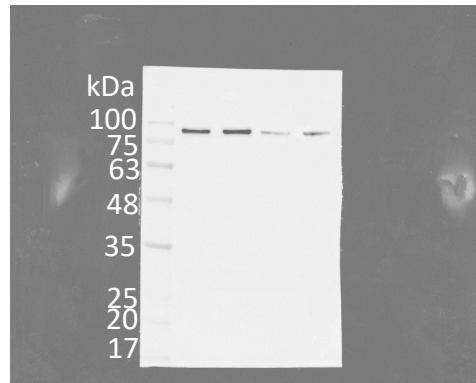


Panel R

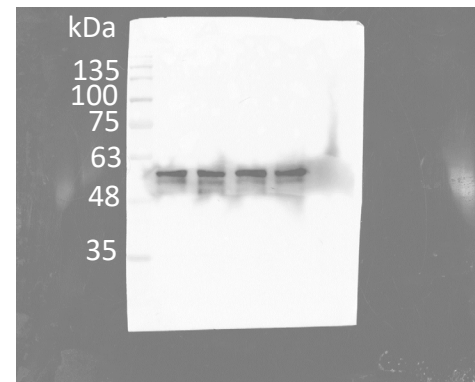
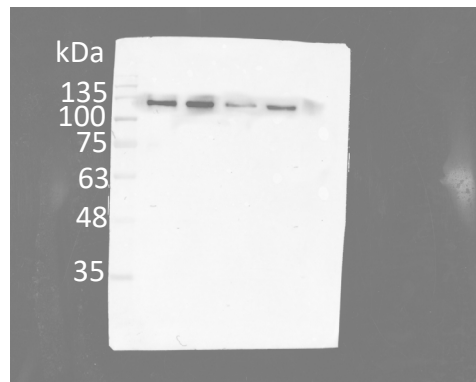
Figure S3. Panel Q, R



Panel D

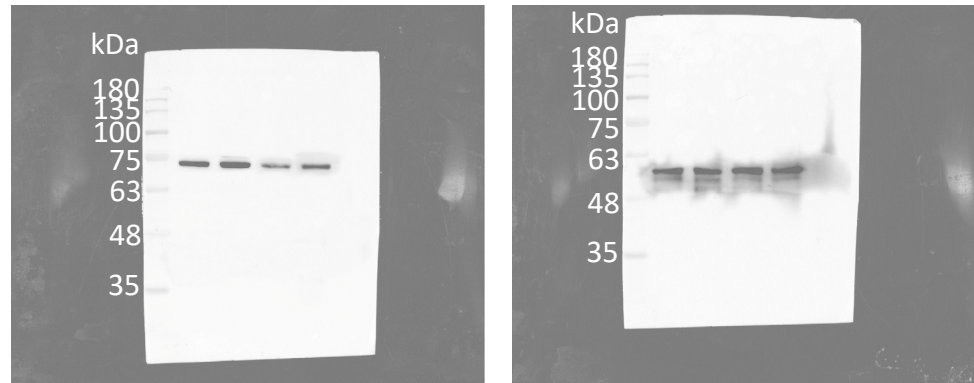


Panel E

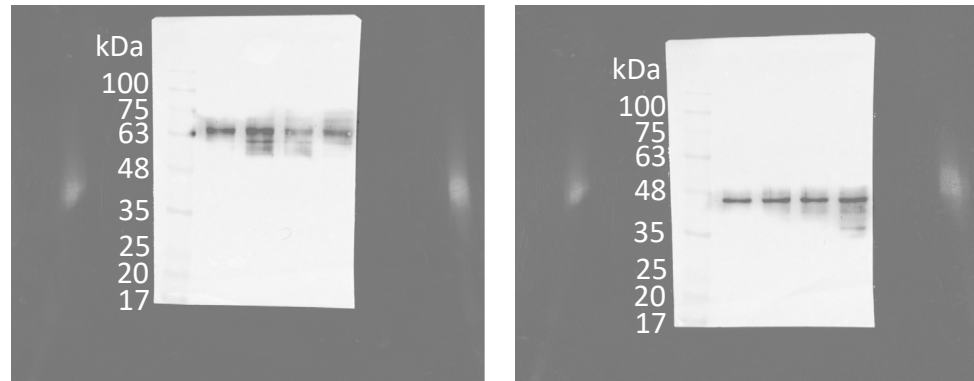


Panel F

Figure S4. Panel D, E, F

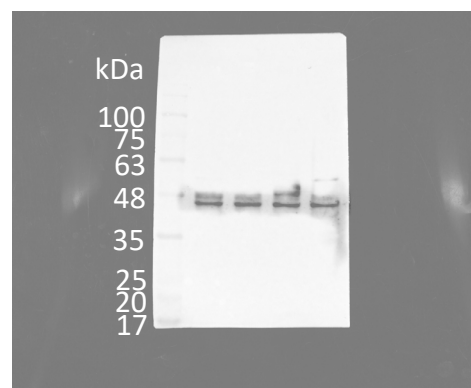
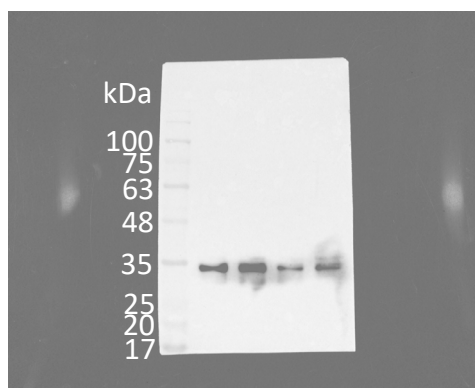


Panel G

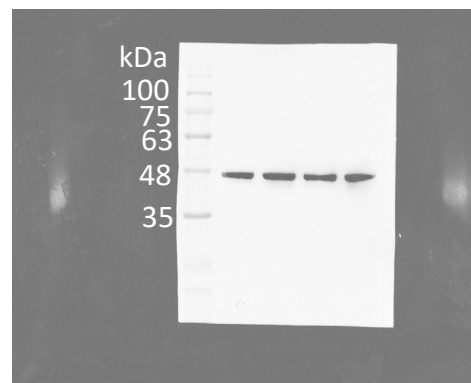
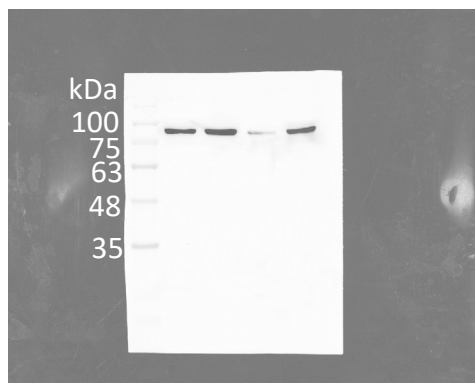


Panel H

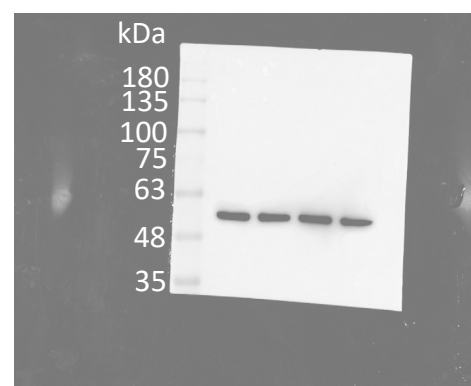
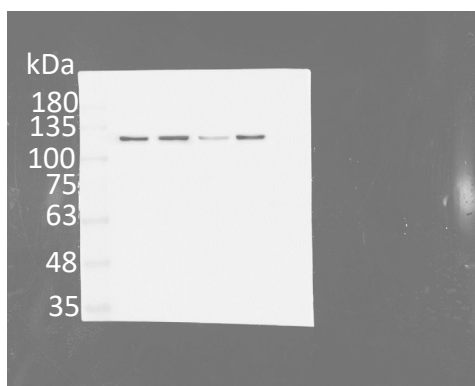
Figure S4. Panel G, H



Panel L

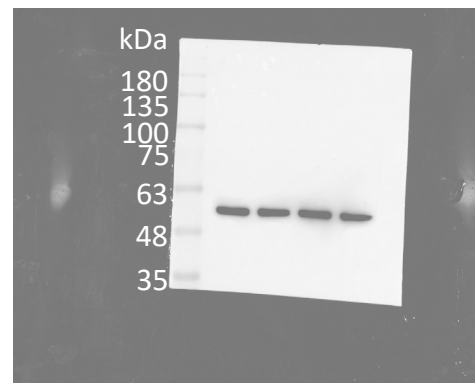
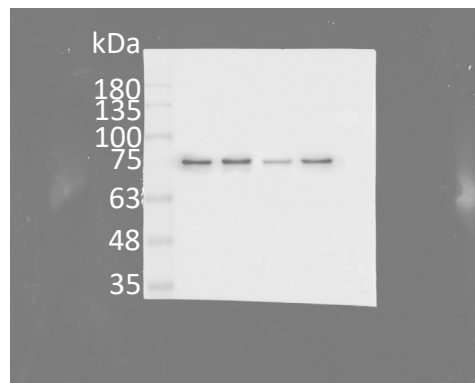


Panel M

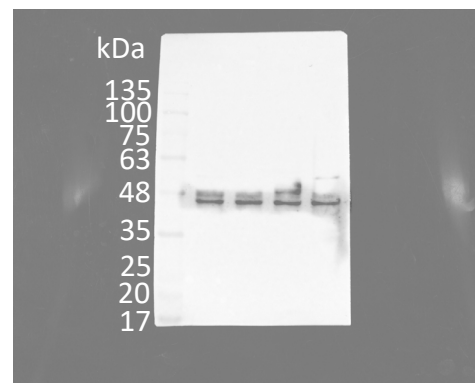
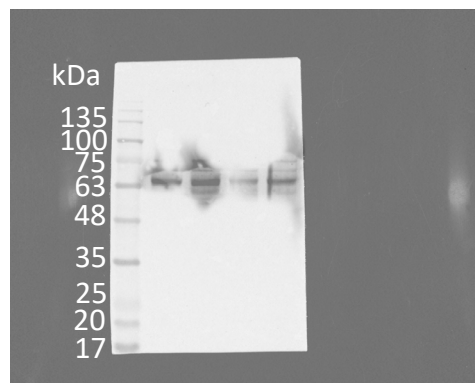


Panel N

Figure S4. Panel L, M, N

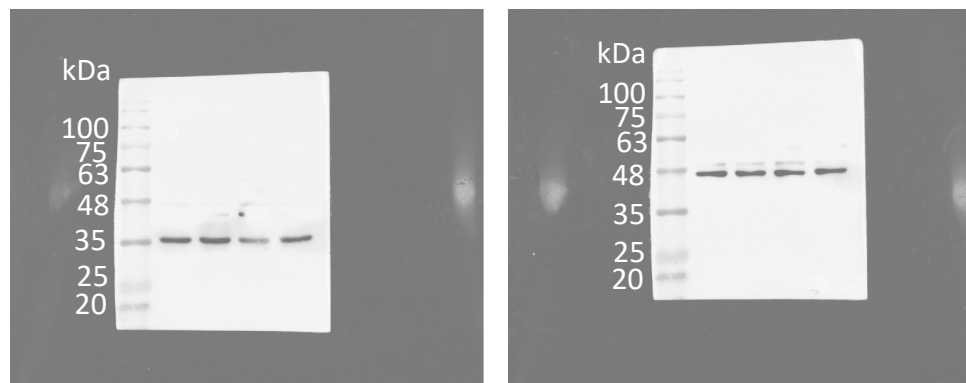


Panel O

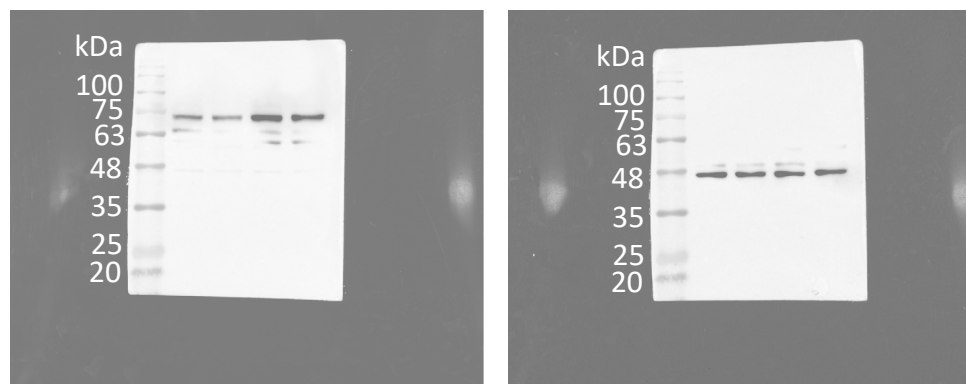


Panel P

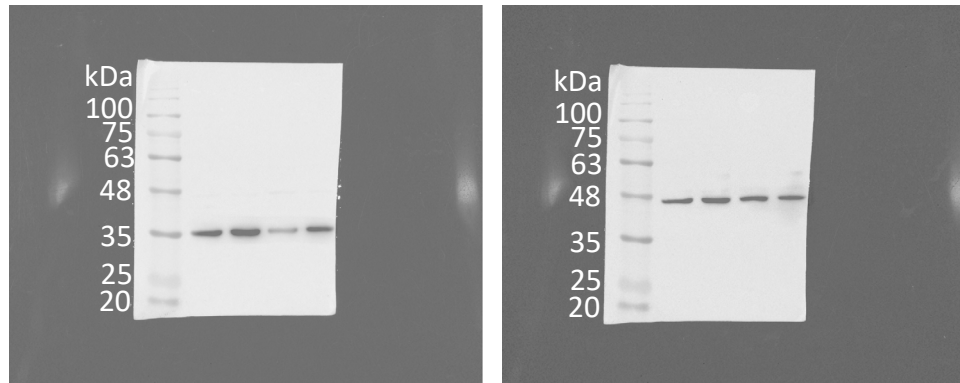
Figure S4. Panel O, P



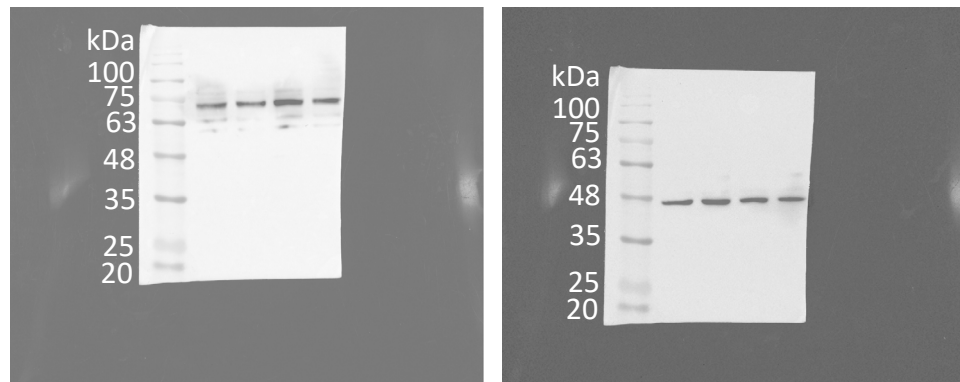
Panel E



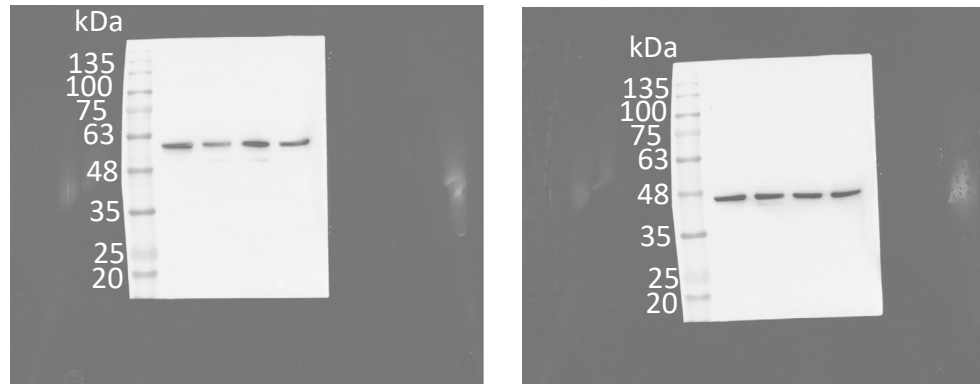
Panel F



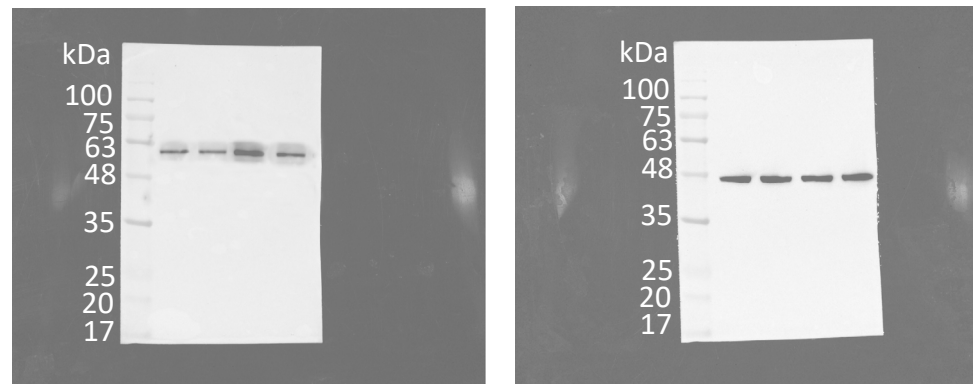
Panel K



Panel L

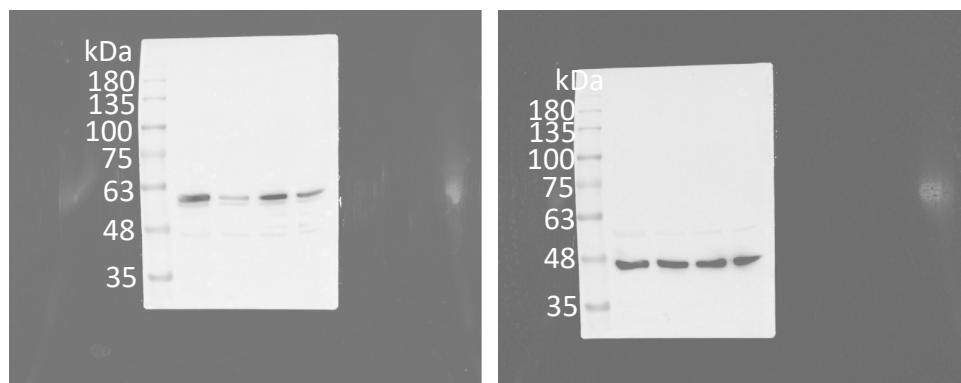


Panel A

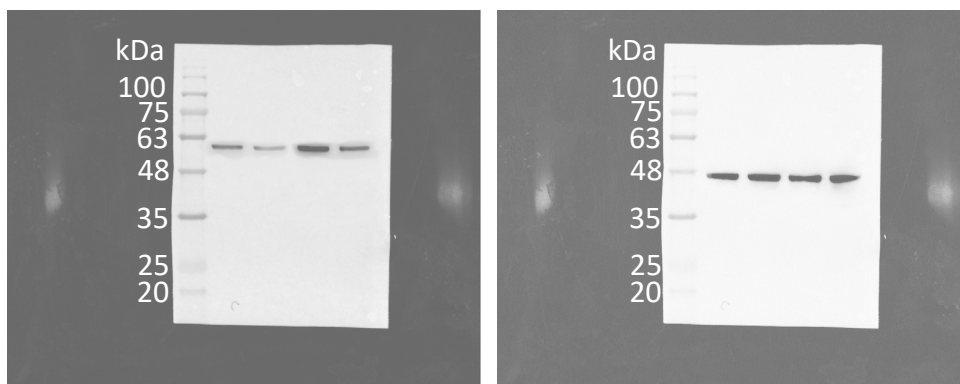


Panel B

Figure S7. Panel A, B



Panel C



Panel D

Figure S7. Panel C, D