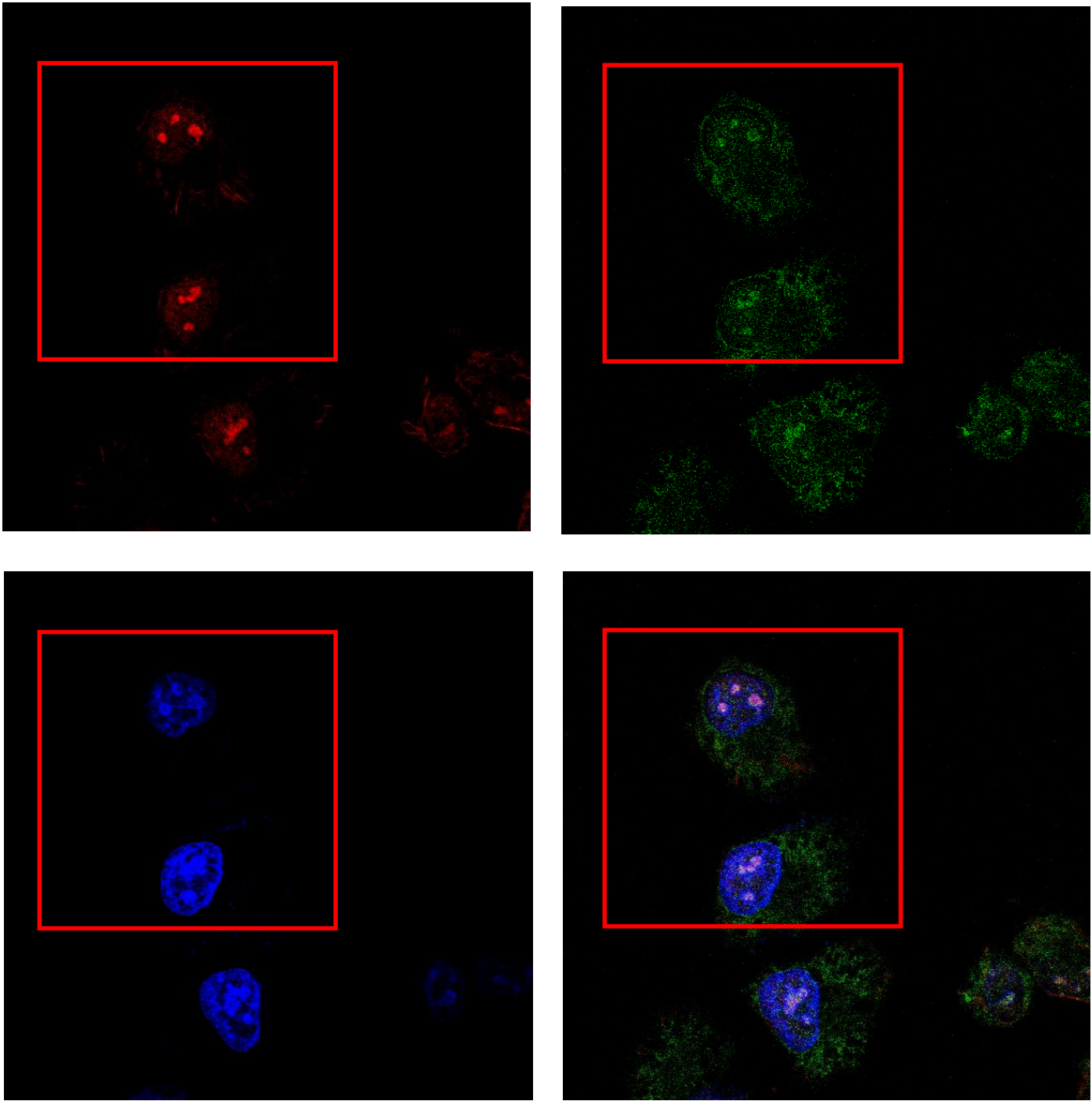
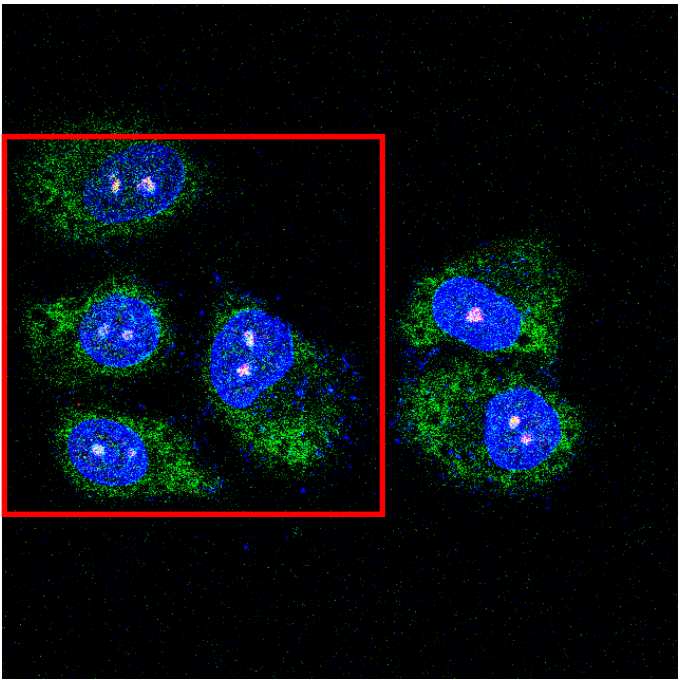
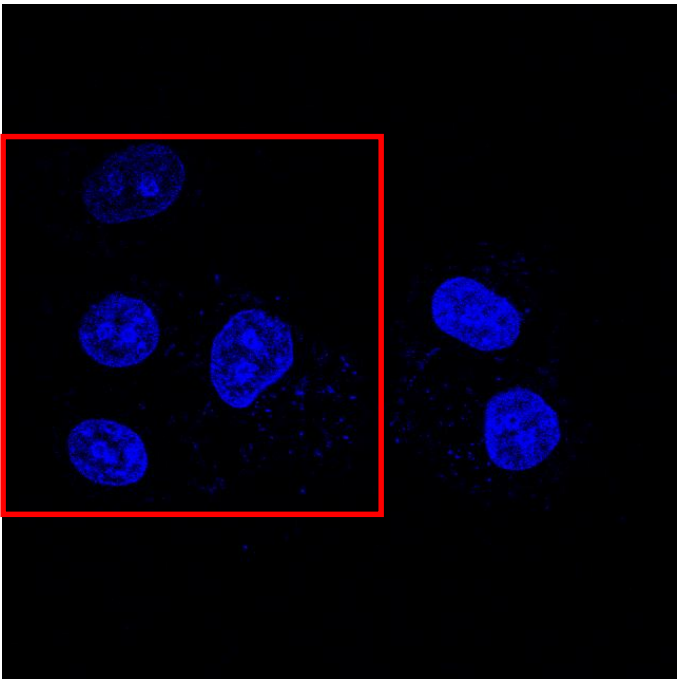
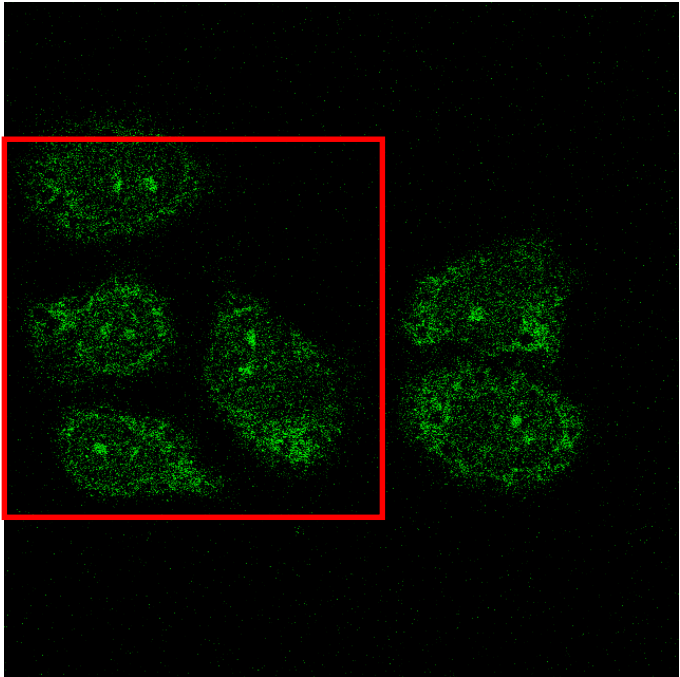
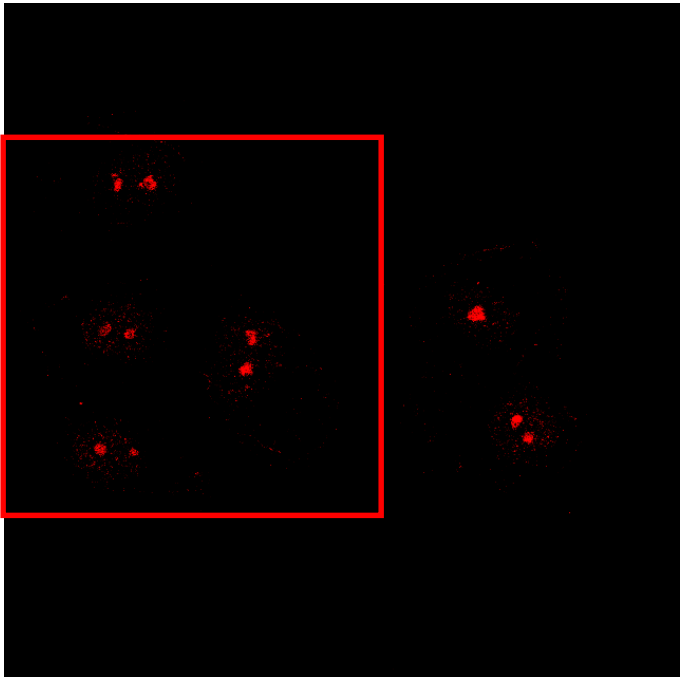


Figure 4E

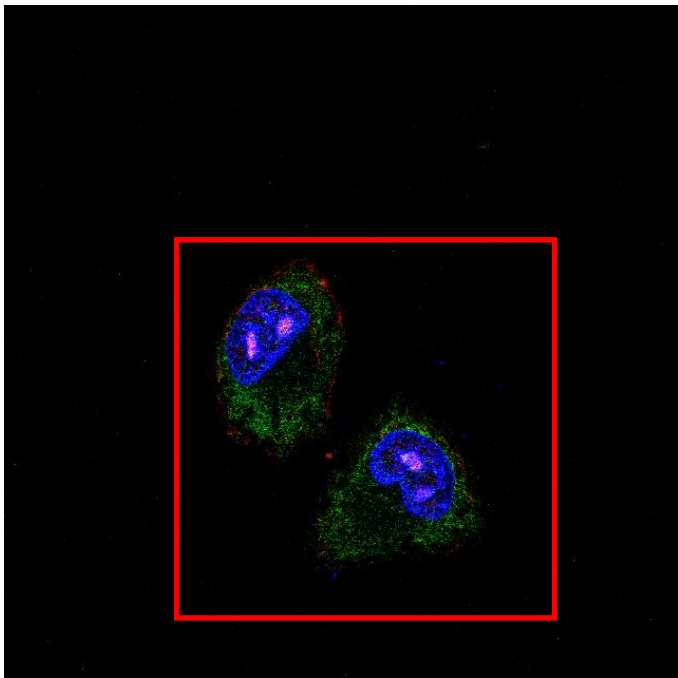
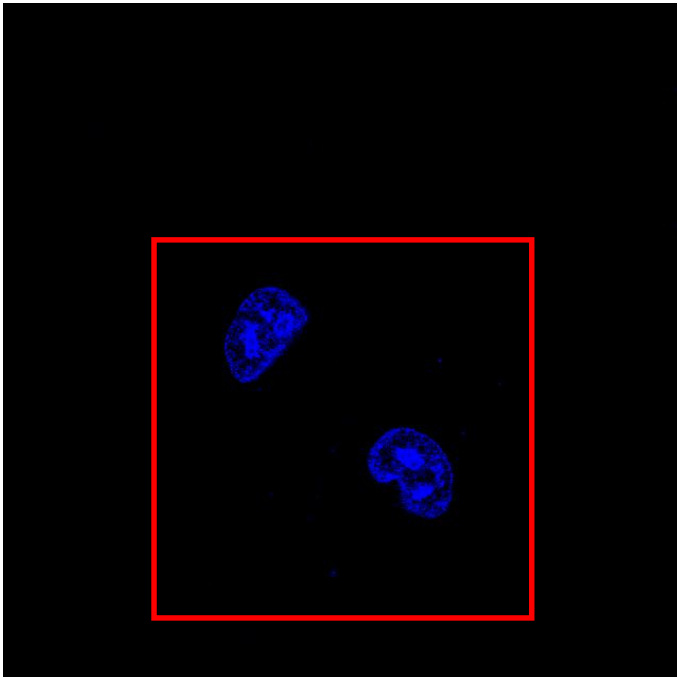
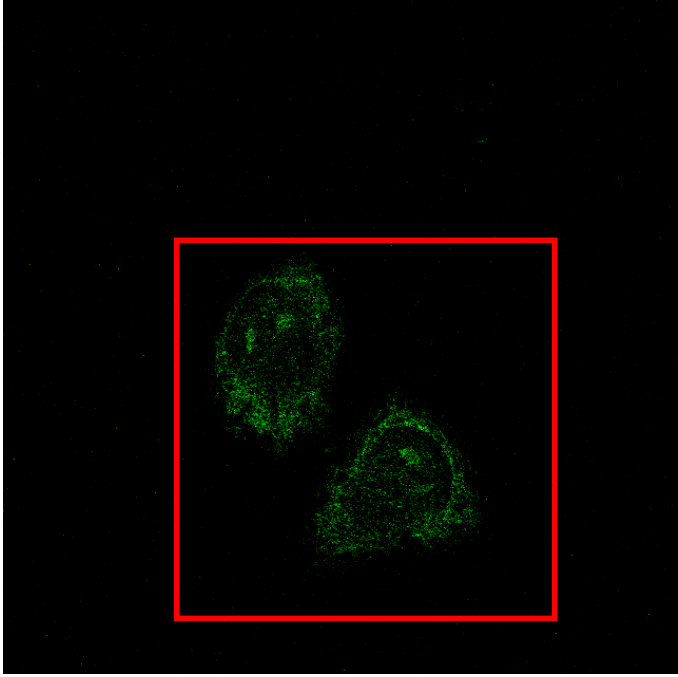
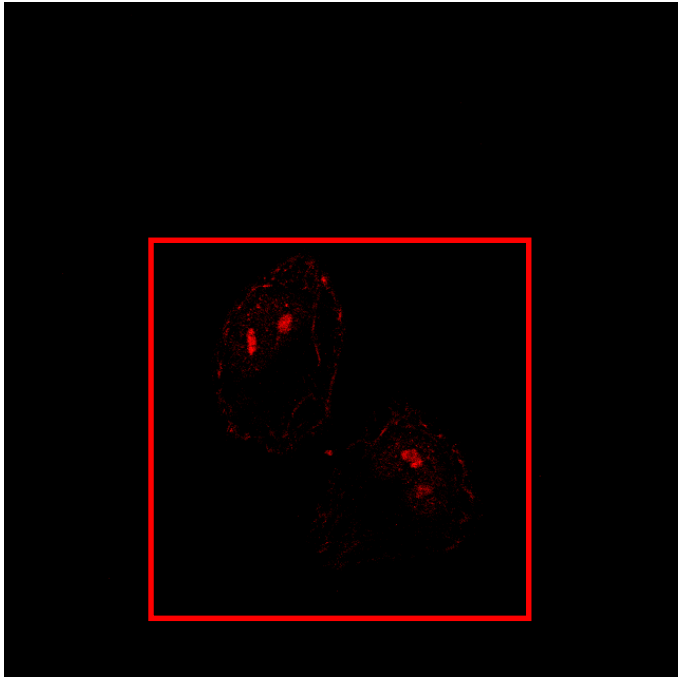
NF45-Mock



NF45-PI



NF90-Mock



NF90-PI

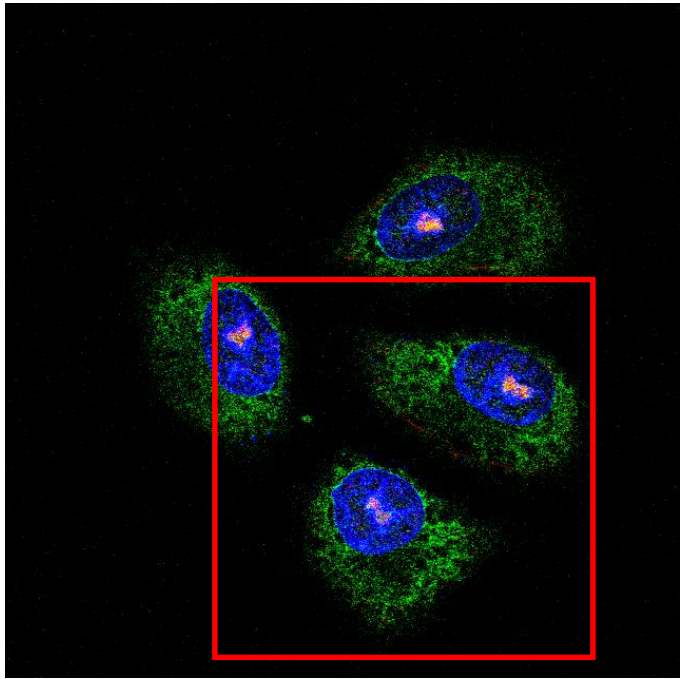
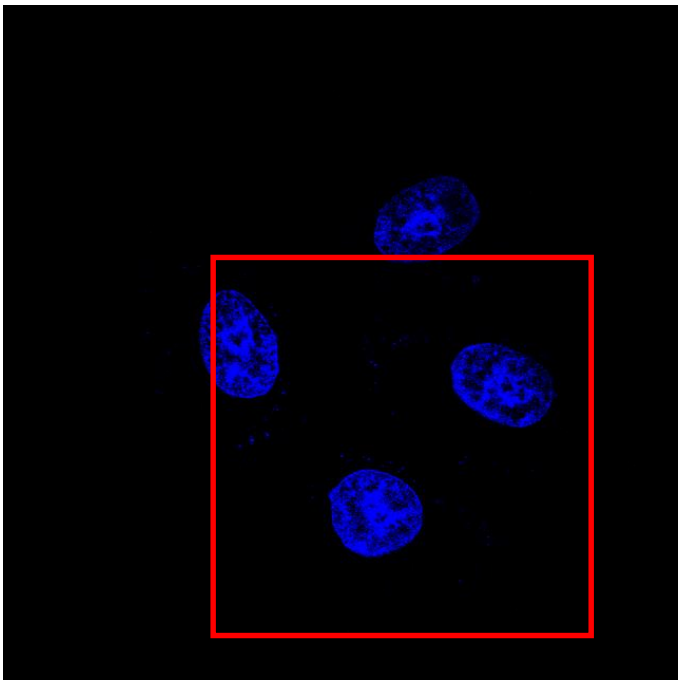
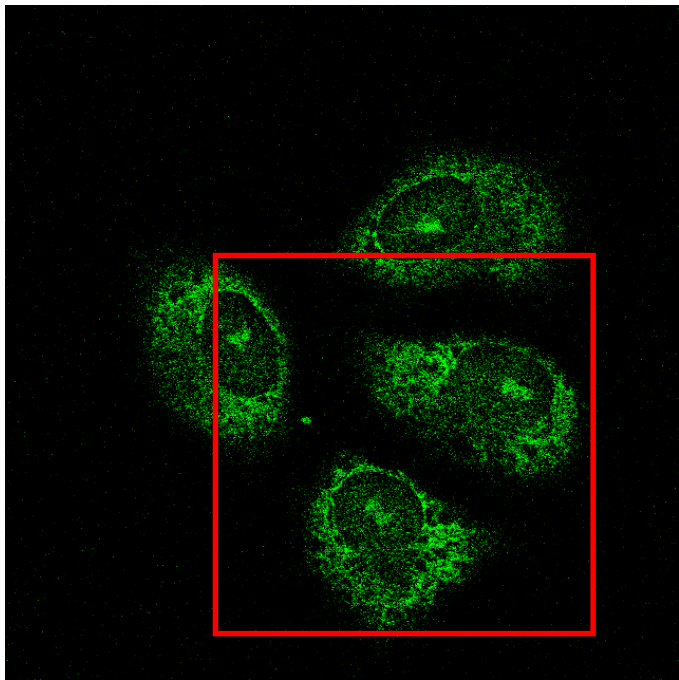
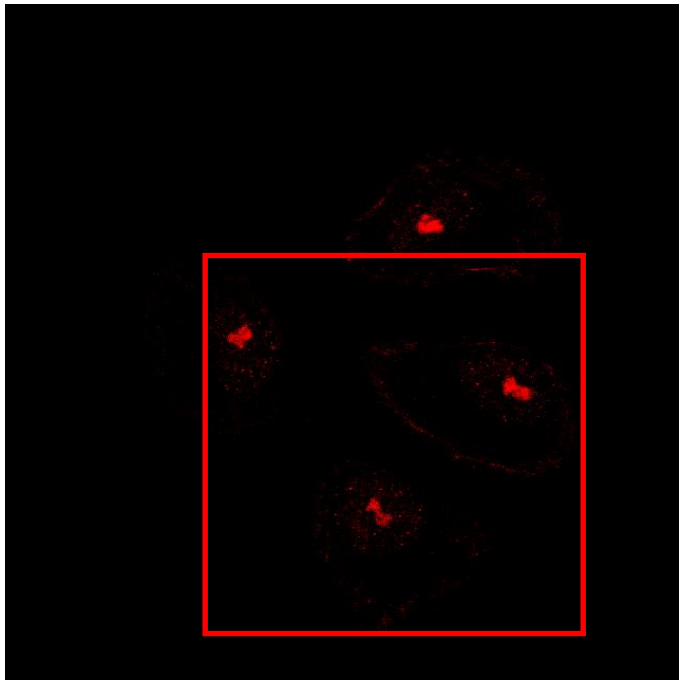
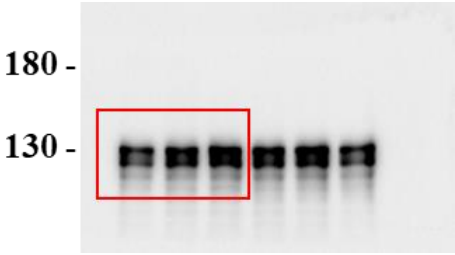
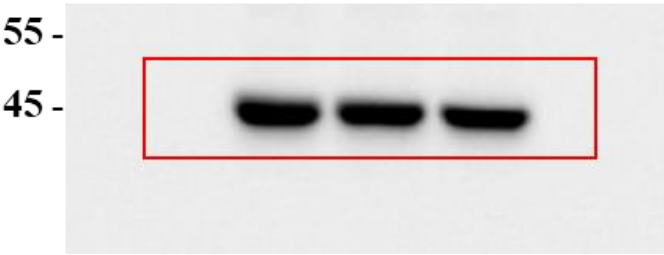


Figure 4F

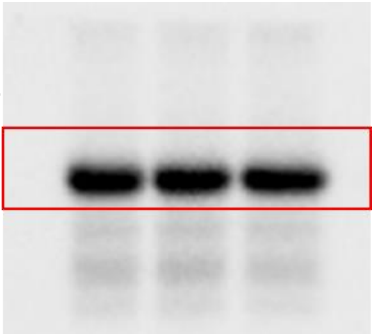
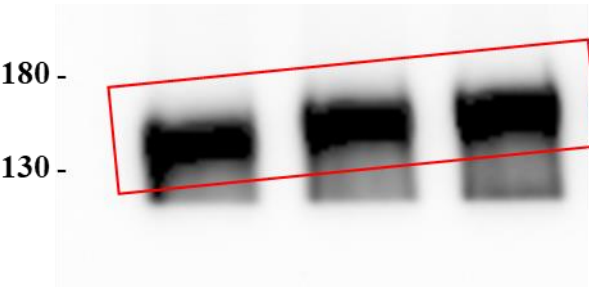
Input:NF45

Input: NFATc1



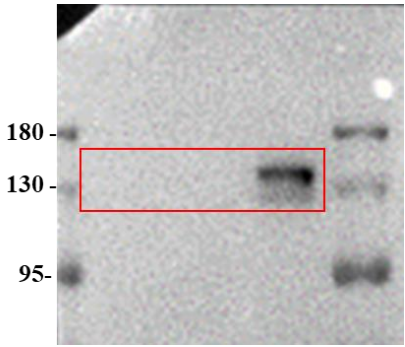
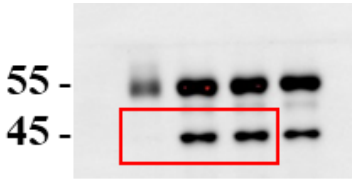
Input: NFATc2

Input: GAPDH

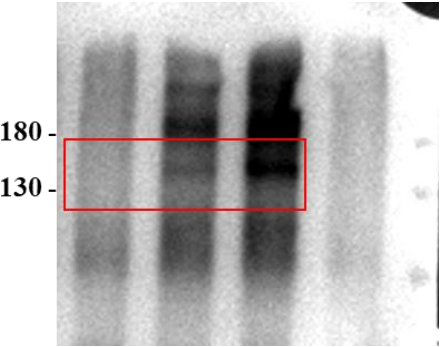


IP:NF45 IB:NF45

IP:NF45 IB:NFATc1



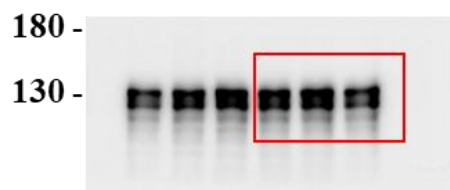
IP:NF45 IB:NFATc2



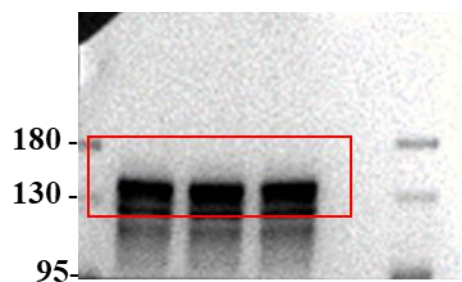
Input: NF90



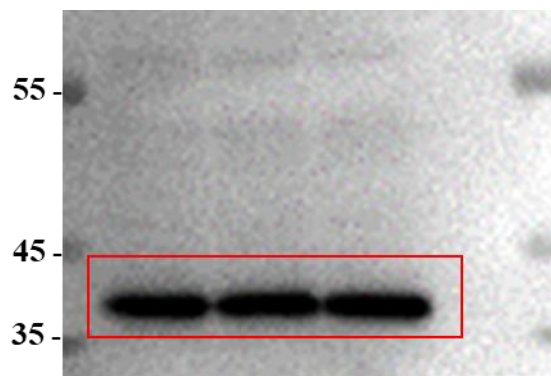
Input: NFATc1



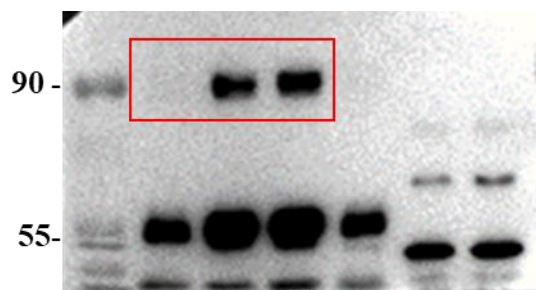
Input: NFATc2



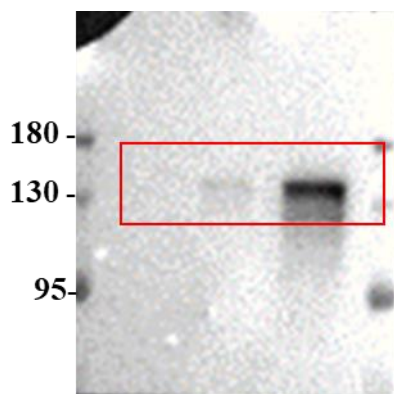
Input: GAPDH



IP: NF90 IB: NF90



IP: NF90 IB: NFATc1



IP: NF90 IB: NFATc2

